

This is a self-archived version of an original article. This version may differ from the original in pagination and typographic details.

Author(s): Akbari-Hasanjani, Reza; Sabbaghi-Nadooshan, Reza; Haghparast, Majid

Title: Toward Quaternary QCA : Novel Majority and XOR Fuzzy Gates

Year: 2022

Version: Accepted version (Final draft)

Copyright: © 2022 the Authors

Rights: CC BY 4.0

Rights url: <https://creativecommons.org/licenses/by/4.0/>

Please cite the original version:

Akbari-Hasanjani, R., Sabbaghi-Nadooshan, R., & Haghparast, M. (2022). Toward Quaternary QCA : Novel Majority and XOR Fuzzy Gates. *IEEE Access*, 10, 38511-38522.
<https://doi.org/10.1109/ACCESS.2022.3165200>

Date of publication xxxx 00, 0000, date of current version xxxx 00, 0000.

Digital Object Identifier 10.1109/ACCESS.2017.Doi Number

Toward Quaternary QCA: Novel Majority and XOR Fuzzy Gates

Reza Akbari-Hasanjani¹, Reza Sabbaghi-Nadooshan¹, Majid Haghparast²

¹Department of Electrical Engineering, Central Tehran Branch, Islamic Azad University, Tehran, Iran

² Faculty of Information Technology, University of Jyväskylä, P.O. Box 35, FI-40014 University of Jyväskylä, Jyväskylä, Finland

Corresponding author: Majid Haghparast (e-mail: majid.m.haghparast@jyu.fi).

Open Access funding provided by University of Jyväskylä (JYU). This work was supported by FinElib, Finland, through the FinELib consortium's agreement with IEEE.

ABSTRACT As an emerging nanotechnology, quantum-dot cellular automata (QCA) has been considered an alternative to CMOS technology that suffers from problems such as leakage current. Moreover, QCA is suitable for multi-valued logic due to the simplicity of implementing fuzzy logic in a way much easier than CMOS technology. In this paper, a quaternary cell is proposed with two isolated layers because of requiring three particles to design this quaternary cell. Moreover, due to the instability of the basic gates, the three particles cannot be placed in one layer. The first layer of the proposed two-layer cell includes a ternary cell and the second one includes a binary cell. It is assumed that the overall polarization of the quaternary QCA (QQCA) cell is determined as the combined polarization of the two layers. The proposed QQCA cell can also be implemented in one layer. Simulations of the QQCA cell are performed based on analytical calculations. Moreover, a majority fuzzy gate, an XOR fuzzy gate, and a crossbar structure are simulated.

INDEX TERMS multi-valued QCA, majority fuzzy gate, XOR fuzzy gate, quaternary, polarization, QQCA

I. INTRODUCTION

Quantum-dot cellular automata (QCA) technology is a candidate to replace CMOS technology in digital circuit designs. CMOS technology has encountered some problems because of the technology scaling. According to Moore's law, the component density of integrated circuits doubles every two years, so scaling of transistors and the subsequent problems in circuit implementation are inevitable [1-8]. QCA, which was first introduced by Lent et al. in the 1990s, is a promising approach for future computing systems which has optimal reliability and performance [2, 4, 9]. Data transfer in QCA cells is based on quantum effects and external electrostatic fields, and no current is transferred in QCA-based circuits. Instead, data is transmitted from one cell to another based on the polarization of cells [1-2].

The first designed binary QCA (bQCA) model contained four potential wells and two electrons [9-13]. The second electron in a bQCA cell stabilizes the cell [14-19]. The first multiple-valued logic (MVL) design in QCA was the

ternary QCA, which consisted of eight potential wells and two electrons [1-3, 20-27].

Multiple-valued logic has drawn attention because of its similarity to fuzzy logic and natural variable description. The advantages of using MVL are the reduction in the number of inputs/outputs, high speed, simplicity, and cost-efficiency. Moreover, MVL can be applied to provide more information on system variables [1, 3].

Many studies have been conducted on ternary cells to design basic gates. To date, however, a QCA cell with more than three values has not been presented. This paper presents a quaternary QCA (QQCA) cell for quantum computation.

II. QQCA

In this section, a QQCA cell model is presented as a three-particle (electron) system. In this system, the electron spin value is equal to 1/2, the quantum number of an electron can be 1/2 or -1/2 [28], and the quantum states describe a three-particle system.

A. Summary of QQCA quantum computing

This section will briefly discuss the concepts of the Hamiltonian matrix and the calculation of polarization in QQCA. As mentioned, each QQCA system can be considered a three-particle system with eight different states based on the quantum number of the particles. Using these quantum states, a state matrix can be provided for the system as given in Eq. 1.

$$S = \begin{pmatrix} \left(\frac{1}{2} \frac{11111111}{22222222} \left| s \right| \frac{1}{2} \frac{11111111}{22222222} \right) & \dots & \dots & \left(\frac{1}{2} \frac{11111111}{22222222} \left| s \right| \frac{1}{2} \frac{11111111}{22222222} \right) \\ \left(\frac{1}{2} \frac{11111111}{22222222} \left| s \right| \frac{1}{2} \frac{11111111}{22222222} \right) & L & L & \left(\frac{1}{2} \frac{11111111}{22222222} \left| s \right| \frac{1}{2} \frac{11111111}{22222222} \right) \\ & M & 0 & M \\ & & 0 & \\ & M & 0 & M \\ \left(\frac{1}{2} \frac{11111111}{22222222} \left| s \right| \frac{1}{2} \frac{11111111}{22222222} \right) & & & \left(\frac{1}{2} \frac{11111111}{22222222} \left| s \right| \frac{1}{2} \frac{11111111}{22222222} \right) \\ \left(\frac{1}{2} \frac{11111111}{22222222} \left| s \right| \frac{1}{2} \frac{11111111}{22222222} \right) & L & L & \left(\frac{1}{2} \frac{11111111}{22222222} \left| s \right| \frac{1}{2} \frac{11111111}{22222222} \right) \end{pmatrix}_{8 \times 8} \quad (1)$$

Moreover, Eq. 1 expresses all states and the spin effect of the particles on each other. This matrix can be separated into three states, each of which represents the state of a particle. Following the separation of the states, the spin angular momentum in the x, y, and z directions can be calculated for each particle from Eqs. 2 to 4 [28].

$$S_z = \langle j, m' | j_z | j, m \rangle = \hbar m \delta_{j',j} \delta_{m',m} \quad (2)$$

$$S_x = \frac{1}{2} ((\hbar\sqrt{j(j+1)-m(m+1)} \delta_{j',j} \delta_{m',m+1}) + (\hbar\sqrt{j(j+1)-m(m-1)} \delta_{j',j} \delta_{m',m-1})) \quad (3)$$

$$S_y = \frac{i}{2} ((\hbar\sqrt{j(j+1)-m(m-1)} \delta_{j',j} \delta_{m',m-1}) - (\hbar\sqrt{j(j+1)-m(m+1)} \delta_{j',j} \delta_{m',m+1})) \quad (4)$$

In the scheme, the calculations are performed for a three-particle system. The calculated spin angular momenta in the three directions for each particle (based on Eq. 1) are algebraically summed, as given in Eqs. 5 to 7.

$$S_x = S_{1x} + S_{2x} + S_{3x} \quad (5)$$

$$S_y = S_{1y} + S_{2y} + S_{3y} \quad (6)$$

$$S_z = S_{1z} + S_{2z} + S_{3z} \quad (7)$$

Based on Eqs. 5 to 7, the spin angular momentum in the x, y, and z directions can be written as follows [28]:

$$S_x = \frac{\hbar}{2} \begin{bmatrix} 0 & 1 & 1 & 2 & 1 & 2 & 2 & 3 \\ 1 & 0 & 2 & 1 & 2 & 1 & 3 & 2 \\ 1 & 2 & 0 & 1 & 2 & 3 & 1 & 2 \\ 2 & 1 & 1 & 0 & 3 & 2 & 2 & 1 \\ 1 & 2 & 2 & 3 & 0 & 1 & 1 & 2 \\ 2 & 1 & 3 & 2 & 1 & 0 & 2 & 1 \\ 2 & 3 & 1 & 2 & 1 & 2 & 0 & 1 \\ 3 & 2 & 2 & 1 & 2 & 1 & 1 & 0 \end{bmatrix} \quad S_y = \frac{\hbar}{2} \begin{bmatrix} 0 & -i & -i & -2i & -i & -2i & -2i & -3i \\ i & 0 & 0 & -i & 0 & -i & -i & -2i \\ i & 0 & 0 & -i & 0 & -i & -i & -2i \\ 2i & i & i & 0 & i & 0 & 0 & -i \\ i & 0 & 0 & -i & 0 & -i & -i & -2i \\ 2i & i & i & 0 & i & 0 & 0 & -i \\ 2i & i & i & 0 & i & 0 & 0 & -i \\ 3i & 2i & 2i & i & 2i & i & i & 0 \end{bmatrix}$$

$$S_z = \frac{\hbar}{2} \begin{bmatrix} 3 & 2 & 2 & 1 & 2 & 1 & 1 & 0 \\ 2 & 1 & 1 & 0 & 1 & 0 & 0 & -1 \\ 2 & 1 & 1 & 0 & 1 & 0 & 0 & -1 \\ 1 & 0 & 0 & -1 & 0 & -1 & -1 & -2 \\ 2 & 1 & 1 & 0 & 1 & 0 & 0 & -1 \\ 1 & 0 & 0 & -1 & 0 & -1 & -1 & -2 \\ 1 & 0 & 0 & -1 & 0 & -1 & -1 & -2 \\ 0 & -1 & -1 & -2 & -1 & -2 & -2 & -3 \end{bmatrix}$$

By eliminating $\frac{\hbar}{2}$ from S_x , S_y , and S_z , the value of δ is

obtained in three directions. Another problem in QCA-based cells is the calculation of the Hamiltonian matrix. In QCA technology, the Hamiltonian matrix is determined using the Hartree-Fock approximation as given in Eq. 8.

$$\hat{H}_j = -\gamma_j \delta_x(j) - \frac{E_k}{2} \delta_z(j) P_j \quad (8)$$

Where γ_j is the tunneling energy; $\delta_x(j)$ and $\delta_z(j)$ are the quasi-Pauli matrices in the x- and z-directions, respectively; P_j is the polarization of QQCA, and E_k is the kink energy value. Using Eq. 8, the value of the Hamiltonian matrix in QQCA can be obtained as described in Ref. [28]:

$$\hat{H}_j = \begin{bmatrix} -\frac{3}{2} E_k P_j & -E_k P_j - \gamma_j & -E_k P_j - \gamma_j & -\frac{1}{2} E_k P_j - 2\gamma_j & -E_k P_j - \gamma_j & -\frac{1}{2} E_k P_j - 2\gamma_j & -\frac{1}{2} E_k P_j - 2\gamma_j & -3\gamma_j \\ -E_k P_j - \gamma_j & -\frac{1}{2} E_k P_j & -\frac{1}{2} E_k P_j - 2\gamma_j & -\gamma_j & -\frac{1}{2} E_k P_j - 2\gamma_j & -\gamma_j & -3\gamma_j & \frac{1}{2} E_k P_j - 2\gamma_j \\ -E_k P_j - \gamma_j & -\frac{1}{2} E_k P_j - 2\gamma_j & -\frac{1}{2} E_k P_j & -\gamma_j & -\frac{1}{2} E_k P_j - 2\gamma_j & -3\gamma_j & -\gamma_j & \frac{1}{2} E_k P_j - 2\gamma_j \\ -\frac{1}{2} E_k P_j - 2\gamma_j & -\gamma_j & -\gamma_j & \frac{1}{2} E_k P_j & -3\gamma_j & \frac{1}{2} E_k P_j - 2\gamma_j & \frac{1}{2} E_k P_j - 2\gamma_j & E_k P_j - \gamma_j \\ -E_k P_j - \gamma_j & -\frac{1}{2} E_k P_j - 2\gamma_j & -\frac{1}{2} E_k P_j - 2\gamma_j & -3\gamma_j & -\frac{1}{2} E_k P_j & -\gamma_j & -\gamma_j & \frac{1}{2} E_k P_j - 2\gamma_j \\ -\frac{1}{2} E_k P_j - 2\gamma_j & -\gamma_j & -3\gamma_j & \frac{1}{2} E_k P_j - 2\gamma_j & -\gamma_j & \frac{1}{2} E_k P_j & \frac{1}{2} E_k P_j - 2\gamma_j & E_k P_j - \gamma_j \\ -\frac{1}{2} E_k P_j - 2\gamma_j & -3\gamma_j & -\gamma_j & \frac{1}{2} E_k P_j - 2\gamma_j & -\gamma_j & \frac{1}{2} E_k P_j - 2\gamma_j & \frac{1}{2} E_k P_j & E_k P_j - \gamma_j \\ -3\gamma_j & \frac{1}{2} E_k P_j - 2\gamma_j & \frac{1}{2} E_k P_j - 2\gamma_j & E_k P_j - \gamma_j & \frac{1}{2} E_k P_j - 2\gamma_j & E_k P_j - \gamma_j & E_k P_j - \gamma_j & \frac{3}{2} E_k P_j \end{bmatrix}$$

In this scheme, quantum measurements are required to calculate the polarization of the input state (from $\delta_z(j)$ in the z-direction) as performed in Eq. 9.

$$P_k = -\langle \delta_{zk} \rangle \quad (9)$$

For the quantum calculations and measurements, the input states must be specified. For the three particles, there are eight states. States $|000\rangle$ and $|111\rangle$ are shown in the following, but all input states are considered in the calculations.

$$|000\rangle = \begin{bmatrix} 1 \\ 0 \\ 0 \\ 0 \\ 0 \\ 0 \\ 0 \\ 0 \\ 0 \end{bmatrix} \quad |111\rangle = \begin{bmatrix} 0 \\ 0 \\ 0 \\ 0 \\ 0 \\ 0 \\ 0 \\ 0 \\ 1 \end{bmatrix}$$

Based on the quantum measurements of input state matrices, the polarization value are $\{-3, -1, -1, 1, -1, 1, 1, 3\}$. In the next section, a two-layer QQCA cell is presented. This cell can also be presented in one layer. The QQCA cell uses a bQCA cell and a ternary QCA (tQCA) cell, which are separated by a distance of 25 nm.

Four particles exist in the QQCA cell. The tQCA cell has two electrons that can provide the polarization $\{-2, 0, 0, 2\}$, and the bQCA cell has another two electrons and four potential wells. However, the bQCA cell can also include a single particle as used in Refs. [14-19]. Because four potential wells exist in the bQCA cell of the QQCA cell, the second electron is considered for cell stability.

Moreover, to obtain the Hamiltonian matrix of the bQCA cell, it must be considered as a single particle system as given in Eq. 10 [27, 29-34].

$$H(j) = \begin{bmatrix} -\frac{E_k}{2} P_j & -\gamma j \\ -\gamma j & \frac{E_k}{2} P_j \end{bmatrix} \quad (10)$$

The polarization of the QQCA cell is the contribution of the polarizations of the bQCA and tQCA cells.

B. Suggested QQCA Cell

The use of quaternary logic increases the speed, requires fewer inputs/outputs, reduces circuit complexity, and allows cost-effective implementation.

A quaternary system requires three particles (electrons) to produce four different polarizations. The polarizations are calculated by solving Eq. (1) and performing quantum measurements in the z-direction, and finally, the polarization values $\{-3, -1, -1, 1, -1, 1, 1, 3\}$ are achieved. Some of the polarizations are duplicated, which are ignored in this study, so the polarizations of the QQCA system are $\{-3, -1, 1, 3\}$ [28].

The quaternary system consists of three particles, which lead to instability in the gate if they are placed in one layer. Therefore, the QQCA cell is presented in two layers, which are separated by 25 nm to reduce and eliminate the electrostatic effects between these two isolated layers. The first layer of the QQCA cell contains a tQCA cell with the polarization $\{-2, 0, 0, 2\}$ from which only -2 and 2 are chosen. The second layer contains a bQCA cell with the polarization $\{-1, 1\}$ [28].

To fabricate QCA cells, electron beam lithography with two resist layers on Si or Si/SiO₂ substrate is used, and quantum dots are deposited on an aluminum substrate, as explained in Ref. [35].

The QQCA cell structure can also be implemented on one layer. The dimensions of less than 10 nm can be achieved using some methods, such as field emission scanning probe

(FE-SP) [36], plasmonic lithography [37], electron beam lithography using polymethylmethacrylate (PMMA) resist [38], and extreme ultraviolet (EUV) interference lithography [39].

In [40], a bQCA cell is fabricated with four quantum dots and two electrons with dimensions of 6 nm×6 nm using Si (100) dangling bonds (DBs). In [41], a bQCA cell is fabricated with two quantum dots and one electron with dimensions of 2 nm using Si DBs.

Figure 1(a) shows the model of the cell in [28], and Figure 1(b) shows the dimensions of the layers [28]. The QQCA model is a three-dimensional structure, so it is difficult to show the designed quaternary gates that use this model. For this reason, the symbolic model is used in the simulation. Figure 1(c) shows the QQCA cell symbol [28]. The QQCA cell polarization consists of the polarizations of both the first (ternary) and the second (binary) layers. Figure 1(d) shows the polarizations of the QQCA cell [28].

III. Integration in applying inputs to the QQCA cell

In the QQCA structure, the input to the cell in each layer is applied through single-electron transistors (SETs). Thus, the input can be integrated into the cell using digital circuits. The purpose of the integration is to apply the calculated polarizations into each of the QQCA cells.

Figure 1(d) shows the polarization of the QQCA cell, which uses only polarizations -2 and 2 in the first (ternary) layer. These polarizations should be combined with the bQCA polarization to obtain the overall polarization of the QQCA cell. In the first layer, polarizations 0(0°) and 0(90°) are considered as intracellular states.

Input and output drivers are used for input and output integration of polarizations in the QQCA cell. The QQCA cell is designed in such a way that the circuits based on this cell can be implemented in one layer using driver and output [28].

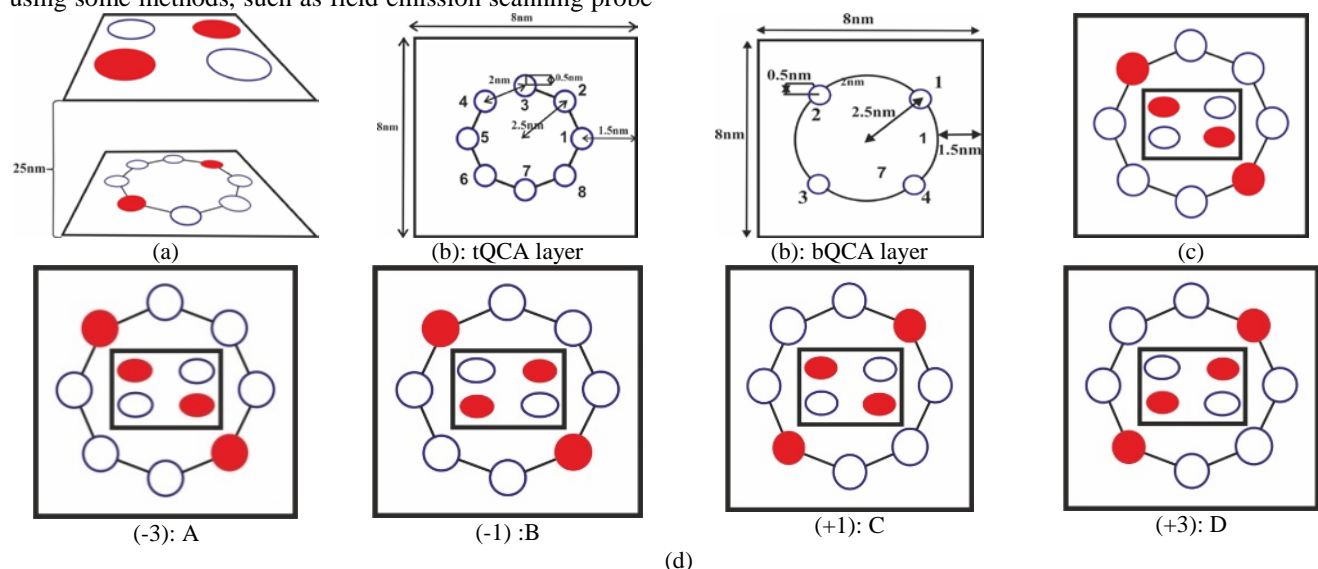


FIGURE 1. (a) QQCA model in [28], (b) specifications of the layers in [28], (c) QQCA cell symbol in [28], and (d) QQCA cell polarization in [28]

A. Input Driver Design

The purpose of driving input and output is their integration based on polarization in QQCA. The driver uses two 2×4 decoders with an Enable, and the input of the bQCA layer is used as the decoder input to create the polarization of the ternary layer at the input. In the presented input driver, the first decoder generates a polarization of -2, and the second decoder generates a polarization of 2. If the Enable pin in the proposed circuit is set to -1, decoder 1 is enabled, while if it is set to 1, decoder 2 is activated. It is notable that for the design of the input driver, SETs are used in the binary layer. The SETs are not activated simultaneously for polarizations -1 and +1. In fact, these transistors are used at the input to apply information to the ternary layer. Figure 2(a) shows the schematic of the input driver for integrating the input into the QQCA cell.

Table 1 shows the truth table of the input driver. In this study, the SETs in the binary layer are used to specify the polarization in the ternary layer.

B. Design of Output Driver

For output integration, the QQCA output driver is used. In this driver, the presence of an electron in the SET channel in each layer is equivalent to 1, while the absence of electron is equivalent to 0.

Two encoders and a decoder are used to realize the integration. To show the polarization in bQCA, two SETs with opposite states are used. These transistors are never activated together, and their simultaneous activation is considered a forbidden state.

The tQCA cell uses four SETs to create polarization in a single cell. These transistors are never activated simultaneously. Figure 2(b) shows the model proposed for the output driver.

Table 2 presents the truth table of the output driver. In this table, the two inputs B and C cannot be identical at the decoder output. This is because B and C are taken from the ternary layer of the QQCA cell. There is an electron in the SETs of QCA cells corresponding to a polarization, while two polarizations never occur simultaneously.

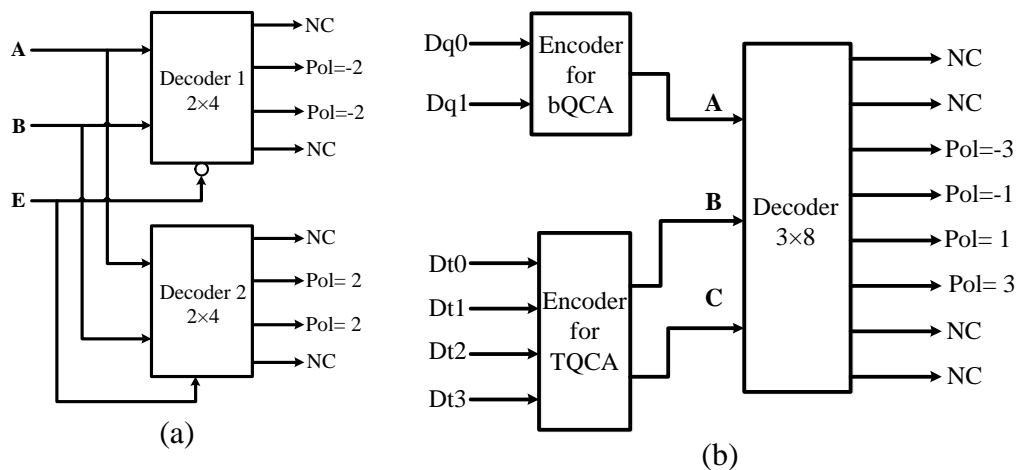


FIGURE 2. Block diagram of (a) the input driver and (b) the output drive in QQCA

TABLE I
Truth table of input driver

Enable	Input binary		Quantum equivalent of input polarization	Output	Connection to transistor corresponding to ternary layer	Quantum equivalent of quaternary polarization
	A	B				
0	0	0	Illegal	Decoder1	-----	-----
	0	1		D1		
0	1	0	1	D2	Transistor of Pol. -2	-1
0	1	1	Illegal	-----	-----	
1	0	0	Illegal	Decoder2	-----	-----
	0	1		D1		
1	1	0	1	D2	Transistor of Pol. 2	3
1	1	1	Illegal	-----	-----	-----

TABLE II
Truth table of output driver

C	B	A	bQCA polarization	tQCA polarization	Q 7	Q 6	Q 5	Q 4	Q 3	Q 2	Q 1	Q 0	Output polarization for QQCA
0	0	0	-1	Illegal	0	0	0	0	0	0	0	1	Illegal
0	0	1	1	Illegal	0	0	0	0	0	0	1	0	Illegal
0	1	0	-1	-2	0	0	0	0	0	1	0	0	-3
0	1	1	1	-2	0	0	0	0	1	0	0	0	-1
1	0	0	-1	2	0	0	0	1	0	0	0	0	1
1	0	1	1	2	0	0	1	0	0	0	0	0	3
1	1	0	-1	Illegal	0	1	0	0	0	0	0	0	Illegal
1	1	1	1	Illegal	1	0	0	0	0	0	0	0	Illegal

IV. ENERGY OF QQCA CELL

Energy in a QQCA cell, like in bQCA and tQCA cells, can be intracellular or extracellular. In the QQCA cell, two layers are used to generate polarization. The first layer contains tQCA, and the second one contains bQCA. The intracellular energy is constant regardless of the polarization. This energy includes internal electrostatic energy, ground state energy, kink energy, and quantum tunneling. Extracellular energy refers to external electrostatic energy. The QQCA cell uses the external electrostatic energy between adjacent cells as described below.

A. External Electrostatic Energy in QQCA Cells

Electrostatic energy is the basis of the operation of QCA-based cells. In these cells, no current flows between two cells during the transmission of information from one cell to another. The electrons in an external electrostatic field interact according to Eq. (11) [28].

$$E_{ij} = \frac{1}{4\pi\epsilon_0\epsilon_r} \sum_{i=0}^m \sum_{j=0}^n \frac{q_i q_j}{d_{ij}} \quad (11)$$

The distance between the two layers in the QQCA cell is considered to be 25 nm to avoid effective electrostatic interaction between the layers. The method presented in Ref. [2] is used to calculate the electrostatic energy between the two cells. For calculating the electrostatic energy of the quaternary cell, the electrostatic energies of the first and second layers are summed. The distances between electrons in two neighboring cells in the first layer (a_{12} , a_{11}), (b_{12} , b_{11}), (b_{12} , a_{11}), and (b_{11} , a_{12}) are calculated, and then, the external electrostatic energy between electrons corresponding to these distances, i.e., E_{e11} , E_{e12} , E_{e13} , and E_{e14} , respectively, are determined.

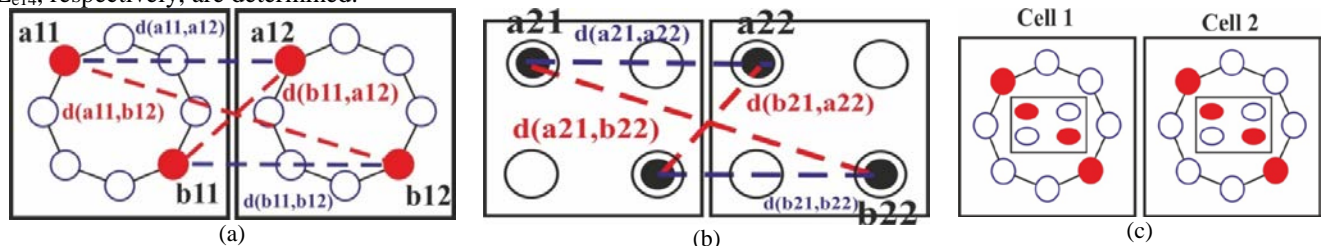


FIGURE 3. Symbolic representation of distances between two QQCA cells in [28]; (a) distances between two ternary cells, (b) distances between binary cells, and (c) symbolic representation of the two QQCA cells

In the next step, the energies resulting from similar labels (E_{e11} and E_{e12}) are summed, as well as those from dissimilar labels (E_{e13} and E_{e14}); the final electrostatic energy of the ternary layer ($E_{tQCA\ layer}$) is obtained by subtracting these two values as given in Eq. (12).

$$E_{tQCA\ layer} = (E_{e11} + E_{e12}) - (E_{e13} + E_{e14}) \quad (12)$$

Figure 3(a) shows how the distances between electrons in two neighboring cells in the first layer are labeled [28].

In the second layer, the same process as in the first one is followed. The distances between electrons in the second layer of two neighboring cells are (a_{22} , a_{21}), (b_{22} , b_{21}), (b_{22} , a_{21}), and (b_{21} , a_{22}). The external electrostatic energies corresponding to these distances are E_{e21} , E_{e22} , E_{e23} , and E_{e24} , respectively. In the next step, the energies resulting from similar labels (E_{e21} and E_{e22}) are summed, as well as those from dissimilar labels (E_{e13} and E_{e14}); the final electrostatic energy of the second binary layer ($E_{bQCA\ layer}$) is obtained by subtracting these two values as given in Eq. (13) [28].

$$E_{bQCA\ layer} = (E_{e21} + E_{e22}) - (E_{e23} + E_{e24}) \quad (13)$$

Figure 3(b) shows how the distances between electrons in two neighboring cells in the second layer are labeled. In the final step, to calculate the electrostatic energy in a QQCA cell, the values $|E_{tQCA\ layer}|$ and $|E_{bQCA\ layer}|$ are algebraically summed as given in Eq. (14). Figure 3(c) illustrates the symbolic representation of the two QQCA cells [28].

$$|E_{QQCA}| = |E_{tQCA\ layer}| + |E_{bQCA\ layer}| \quad (14)$$

V. Clock in QQCA

The clock in QQCA is the same as the clock in bQCA, with the following four phases:

- Switch
- Hold
- Release
- Relax

In the zero or Switch phase, the potential of the barrier increases slowly, which reduces the kinetic energy of the electrons in each layer. In the first or Hold phase, the potential of the barrier reaches its maximum, and the kinetic energy of the electrons in each layer is almost zero. In the second or Release phase, the potential of the barrier begins to slowly decrease and the kinetic energy of the electrons in each layer begins to increase. In the fourth or Relax phase, the potential of the barrier reaches its lowest point and electrons can easily tunnel into any layer of the QQCA cell. Of course, tunneling only occurs inside the cell and there is no tunneling from one cell to another. The clocking in QQCA is shown in Fig. 4 [28].

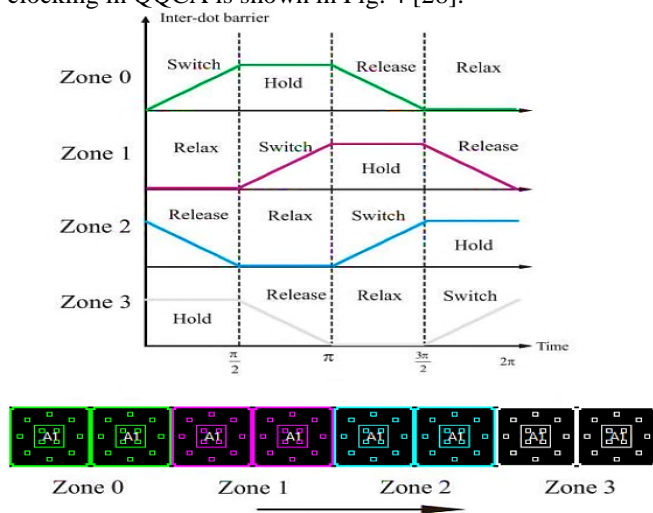


FIGURE 4. Clock in QQCA [28]

VI. Power Dissipation in QQCA Cell

The power dissipation in a QQCA cell can be examined from two perspectives. First, the power consumption of a three-particle system for quantum computing, and second, the power dissipation of the QQCA cell [28]. Equation 15 calculates power dissipation in a cell in QCA technology.

$$P_{diss} = \frac{dE}{dt} = -\frac{1}{\tau}(E - E_{ss}) \quad (15)$$

where τ is the time of the Relax phase and change in polarization, E is the expected energy calculated from the measurement of the Hamiltonian matrix (due to the movement of electrons in the potential wells calculated in Eq. 16), and E_{ss} is the thermal equilibrium energy calculated from Eq. 17 [28, 29, 31].

$$E = \frac{\hbar}{2} \Gamma \lambda \quad (16)$$

$$E_{ss} = \frac{\hbar}{2} \Gamma \lambda_{ss} = -\left(\frac{\hbar}{2} \left| \Gamma \right| \right) \tanh(\Delta) \quad (17)$$

where \hbar is the reduced Planck constant, Γ is the magnitude of the energy vector in the x , y , and z directions, λ is the coefficient vector of the density matrix, and λ_{ss} is the steady-state coefficient. Equations 18 to 20 have to be calculated for obtaining these parameters [28, 29, 31]. The power dissipation of the QQCA cell is shown in Fig. 5.

$$\Gamma_i = \frac{Tr[\mathbf{H}\delta_i]}{\hbar} \quad i=x,y,z \quad (18)$$

$$\lambda = \langle \delta_i \rangle \quad i=x,y,z \quad (19)$$

$$\lambda_{ss} = -\frac{\Gamma}{\left| \Gamma \right|} \tanh(\Delta) \quad , \quad \Delta = \frac{\hbar \left| \Gamma \right|}{2K_b T} \quad (20)$$

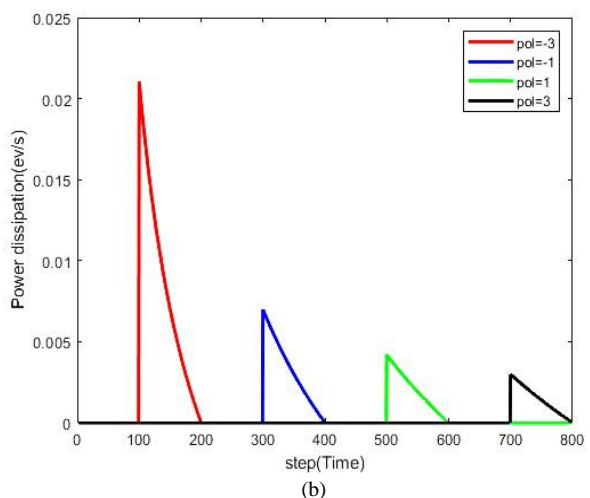
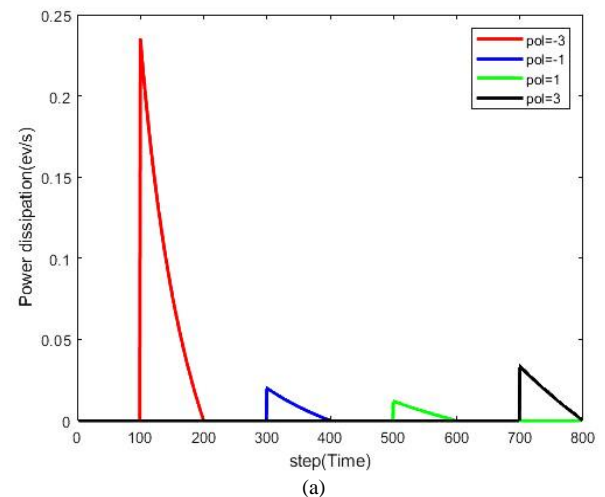


FIGURE 5. a) power dissipation in QQCA for quantum calculation and b) power dissipation of the QQCA [28].

VII. PROPOSED MAJORITY FUZZY GATE STRUCTURE IN QQCA

In the quantum calculation of QQCA, the polarization is determined from the Hartree-Fock approximation in the form $\{-3, -1, 1, 3\}$. A QQCA cell is a three-particle system, and if the three particles are considered in one layer, the quantum numbers of two identical particles cause instability in the cell and incorrect output of the logic gates.

As explained in Section 2, the QQCA cell has two layers, with the tQCA cell in the first layer and the bQCA cell in the second layer. QQCA polarization was obtained from the algebraic sum of the polarization of these two layers. The purpose of this section is to design QQCA majority fuzzy gates similar to bQCA ones in a single-phase clock.

In the QQCA cell, the circular radius of the potential wells is 2.5 nm, and their distance from the wall is 1.5 nm. The wells in the first and second layers are at 45- and 90-degree angles, respectively.

First, all the states $\{A B C D\}$ are applied to the inputs of the majority fuzzy gate structure, as shown in Fig. 6. The standard majority fuzzy gate is shown in Fig. 6a, and the diagonal majority fuzzy gate is shown in Fig. 6b. The responses of the two proposed gates as states $\{A, B, C, D\}$ are given in Table 3. The proposed majority fuzzy gates are the basis for the design of AND and OR gates, which are not plotted for the sake of brevity [42-44].

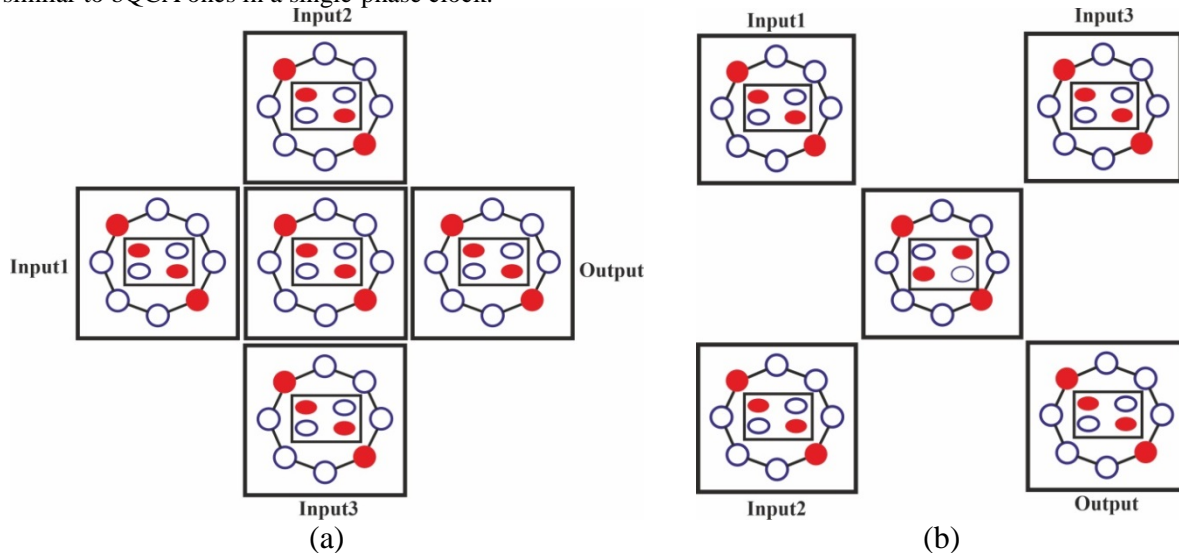


FIGURE 6. (a) standard and (b) diagonal QQCA majority fuzzy gates

Table III
QQCA Majority Gate Output

<i>In1</i>	<i>In2</i>	<i>In3</i>	<i>Out</i>	<i>In1</i>	<i>In2</i>	<i>In3</i>	<i>Out</i>	<i>In1</i>	<i>In2</i>	<i>In3</i>	<i>Out</i>	<i>In1</i>	<i>In2</i>	<i>In3</i>	<i>Out</i>
A	A	A	A	B	A	A	A	C	A	A	A	D	A	A	A
A	A	B	A	B	A	B	B	C	A	B	A	D	A	B	B
A	A	C	A	B	A	C	A	C	A	C	C	D	A	C	C
A	A	D	A	B	A	D	B	C	A	D	C	D	A	D	D
A	B	A	A	B	B	A	B	C	B	A	A	D	B	A	B
A	B	B	B	B	B	B	B	C	B	B	B	D	B	B	B
A	B	C	A	B	B	C	B	C	B	C	C	D	B	C	D
A	B	D	B	B	B	D	B	C	B	D	D	D	B	D	D
A	C	A	A	B	C	A	A	C	C	A	C	D	C	A	C
A	C	B	A	B	C	B	B	C	C	B	C	D	C	B	D
A	C	C	C	B	C	C	C	C	C	C	C	D	C	C	C
A	C	D	C	B	C	D	D	C	C	D	C	D	C	D	D
A	D	A	A	B	D	A	B	C	D	A	C	D	D	A	D
A	D	B	B	B	D	B	B	C	D	B	D	D	D	B	D
A	D	C	C	B	D	C	D	C	D	C	C	D	D	C	D
A	D	D	D	B	D	D	D	C	D	D	B	D	D	D	D

VIII. QQCA SIMULATOR FOR CIRCUIT SIMULATION

In this section, QQCA Sim software is reviewed based on calculations. This software was first introduced by our research team and its functions are similar to computer programs such as tQCA Sim [2] and QCA Designer. The QQCA Sim software can be used to simulate QCA-based quaternary circuits. In this software, the cell size is considered to be 8 nm×8 nm. According to Ref. [45], size reduction in QCA technology improves cell performance, and we examine this improvement in metal QCA technology. Based on calculations, the polarization in a quaternary system is calculated as $\{-3, -1, 1, 3\}$. The combination of polarizations of bQCA and tQCA gives the overall polarization.

In this study, a majority fuzzy gate is first proposed by using the QQCA cell and by performing simulation. Then, this gate is used as the basis for designing AND, OR, and NOT basic gates. An XOR fuzzy gate is implemented to verify the performance of the presented majority fuzzy gate (AND, OR, and NOT basic gates are marked in Fig. 8, which shows the implemented XOR fuzzy gate). The majority fuzzy gate based on the QQCA cell is simulated by the QQCA Sim software version 1.0.0.1.

A. Majority Fuzzy Gate

Based on the QQCA cell model, the structure of the majority fuzzy gate presented in this study is similar to that of the bQCA or tQCA majority fuzzy gate because the two cell models are used in the design. In the majority fuzzy gate, the output response is the state with the most repetitions at the input. To conform to the QCA standard, the majority fuzzy gate is simulated in one clock phase. Figure 7 shows the majority fuzzy gate and results provided by QQCA Sim software. In this figure, the input cell is blue, the middle cell is green, and the output cell is yellow. The comparison of the majority fuzzy gate results in Table 3, and the simulation results show a good agreement, and this indicates the accuracy of the calculations.

The QQCA-based gates were simulated in one phase clock to fully conform to the QCA standard.

In the majority fuzzy gate, if one of the inputs remains constant in State A, the quaternary AND function can be implemented; if one of the inputs remains constant in State D, the quaternary OR function can be implemented. To date, quaternary majority fuzzy gates have not been studied. Therefore, the proposed gate with Min and Max functions,

which are similar to the quaternary AND and OR functions, is compared with its counterparts in CMOS and CNTFET technologies in terms of latency and complexity as shown in Table 4.

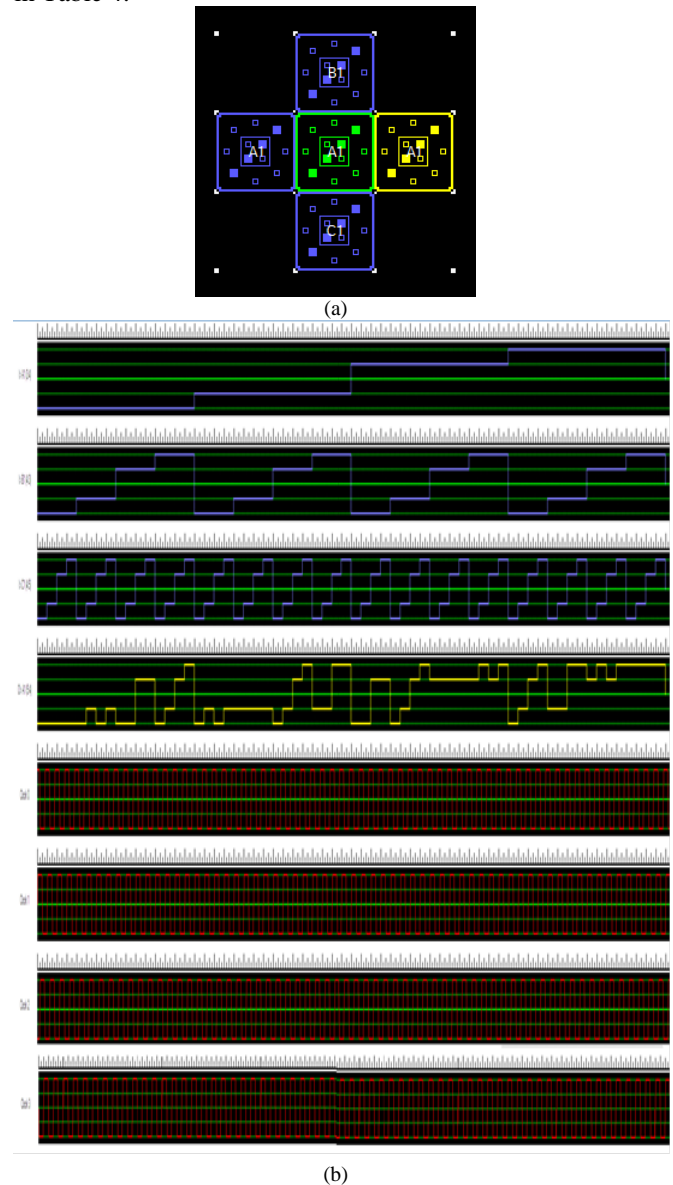


FIGURE 7. Majority fuzzy gate and results provided by QQCA Sim software; (a) majority fuzzy gate, (b) output waveform of the majority fuzzy gate

Table IV
Comparison of the proposed majority fuzzy gate with Min and Max functions

Gate	Complexity	Latency (s)
Proposed majority fuzzy gate	5	0.25×10^{-12}
NOT Gate + NMIN Gate (CNTFET) [46]	26	34.9×10^{-12}
NOT Gate + NMAX Gate (CNTFET) [46]	26	34.92×10^{-12}
NOT Gate + NMIN Gate (CNTFET) [47]	20	21.85×10^{-12}
NOT Gate + NMAX Gate (CNTFET) [47]	20	16.82×10^{-12}
NOT Gate + NMIN Gate (CMOS) [48]	18	733×10^{-12}
NOT Gate + NMAX Gate (CMOS) [48]	18	704×10^{-12}

B. Implementation of XOR Fuzzy Function in QQCA

The XOR operator consists of a combination of basic operators. A quaternary XOR fuzzy gate has two inputs and in quaternary algebra produces one output according to Eq. 15. In this equation, A and B are the inputs of the XOR fuzzy gate, and y is the output of the gate. To design the quaternary XOR fuzzy gate, two AND gates, two NOT gates, and one quaternary OR gate are needed. The designed quaternary XOR fuzzy gate using the QQCA cell is shown in Fig. 8.

$$y = AB' + A'B \quad (15)$$

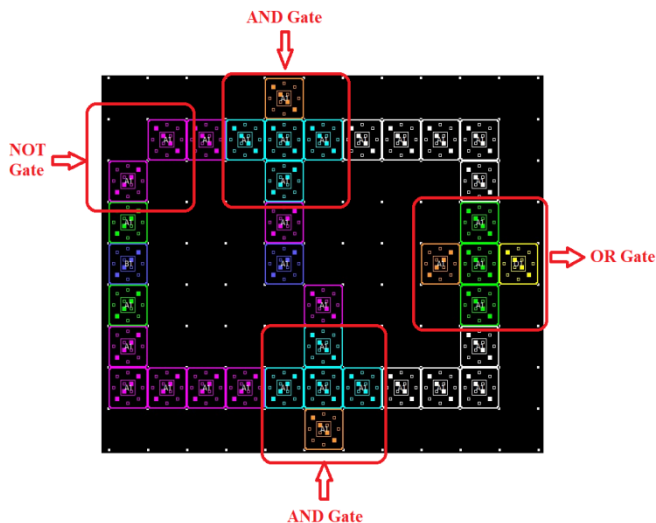


FIGURE 8. Quaternary XOR fuzzy gate

The simulation results and the truth table of the XOR fuzzy gate, which uses 38 QQCA cells, are shown in Fig. 9. The results demonstrate the correct performance of the proposed XOR fuzzy gate as compared to Refs. [42-43].

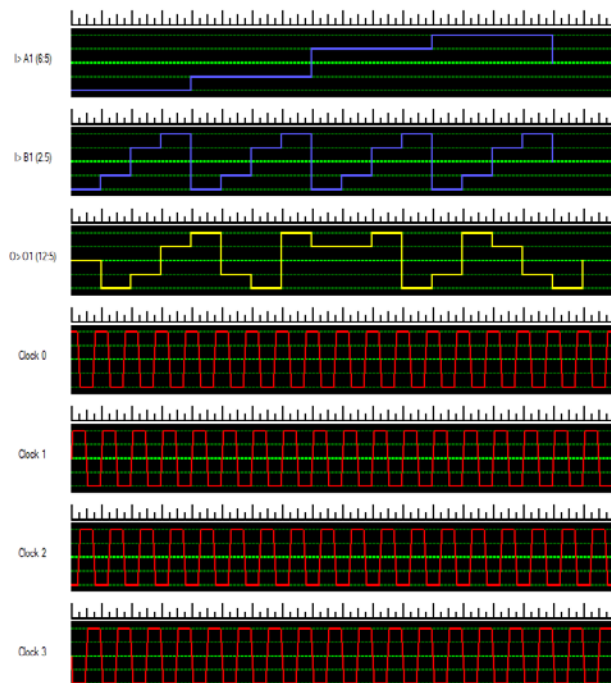
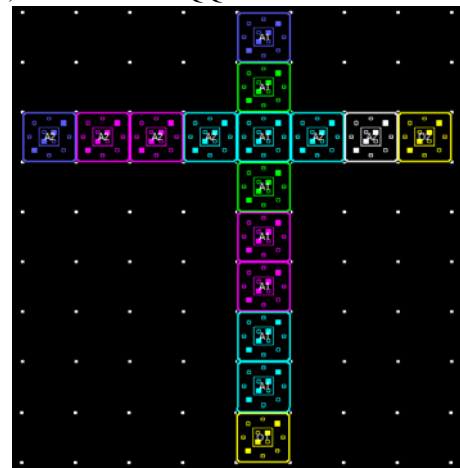


FIGURE 9. The output waveform from simulation results

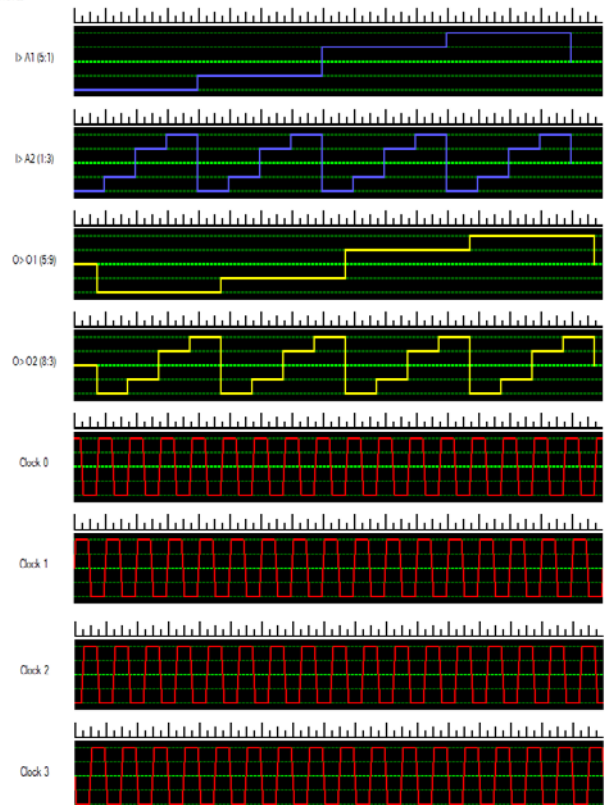
C. Implementation of Crossbar with QQCA cell

This section simulates a crossbar using a QQCA cell. In QCA-based circuits, crossing wires are one of the most important issues. Crossbar designs using QQCA and bQCA cells are the same. In a crossbar design, the wire-crossing point should have two different clock phases so that the signals on the crossing wires do not interfere with each other. Figure 10 shows the QQCA-based crossbar, its simulation results, and its truth table. In this figure, input signal A1 is transmitted to output O1 and input signal A2 to output O2; this indicates the proper operation of the crossbar, which uses the QQCA cell.



(a)

Waves



(b)

FIGURE 10. (a) Structure of cross over, (b) output waveform

IX. DISCUSSION AND CONCLUSION

In this study, basic quaternary quantum gates were investigated. These gates were based on the QQCA cell model, which is a three-particle structure. Some polarization values were repetitive, so they were omitted, and the final polarization set was obtained as $\{-3 -1 1 3\}$. Since including more than two electrons in one layer could result in cell instability, the QQCA cell was presented in two layers to design stable gates. In the QQCA cell, the first layer uses a ternary cell and the second layer uses a binary cell in two-particle and one-particle structures, respectively. In the QQCA cell, the two layers are separated by 25 nm to avoid effective electrostatic interaction between the layers. The size of the cell is 8 nm \times 8 nm, and the overall polarization of the QQCA cell can be determined as the sum of the polarizations of the layers, as follows: For polarization -3, the polarizations -2 and -1 correspond, respectively, to the ternary and binary layers are summed; for obtaining -1, polarizations -2 and 1 are summed; for obtaining 1, polarizations 2 and -1 correspond, respectively, to the ternary and binary layers are summed; finally, for obtaining 3, polarizations 2 and 1 are summed. Using the QQCA cell, a majority fuzzy gate, an XOR fuzzy gate, and a crossbar structure were designed. The results of the QQCA cell design are in good agreement with those of the quaternary gates designed in CMOS technology, yet designing a logic gate in QQCA is much easier than in CMOS technology. The QQCA-based gates were simulated in one phase clock to fully conform to the QCA standard. According to the calculations of the basic quaternary gates, a software was presented to perform simulations in quaternary QCA technology.

Reference

- [1] S. M. Mohaghegh, R. Sabbaghi-Nadooshan, and M. Mohammadi, "Designing ternary quantum-dot cellular automata logic circuits based upon an alternative model," *Computers & Electrical Engineering*, vol. 71, pp. 43-59, 2018.
- [2] S. Mohammadi Mohaghegh, R. Sabbaghi-Nadooshan, and M. Mohammadi, "Innovative model for ternary QCA gates," *IET Circuits, Devices & Systems*, vol. 12, no. 2, pp. 189-195, 2018.
- [3] S. Tabrizchi, A. Panahi, F. Sharifi, H. Mahmoodi, and A.-H. A. Badawy, "Exploring Energy-Efficient Ternary Inexact Multipliers Using CNT Transistors," 2020.
- [4] A. K. Pramanik, D. Bhowmik, J. Pal, P. Sen, A. K. Saha, and B. Sen, "Towards the realization of regular clocking-based QCA circuits using genetic algorithm," *Computers & Electrical Engineering*, vol. 97, p. 107640, 2022.
- [5] F. Salimzadeh and S. R. Heikalabad, "Design of a novel reversible structure for full adder/subtractor in quantum-dot cellular automata," *Physica B: Condensed Matter*, vol. 556, pp. 163-169, 2019.
- [6] E. AlKaldy, A. H. Majeed, M. S. Zainal, and D. M. Nor, "Optimum multiplexer design in quantum-dot cellular automata," *arXiv preprint arXiv:2002.00360*, 2020.
- [7] M. Hayati and A. Rezaei, "Design of a new Optimized Universal Logic Gate for quantum-dot cellular automata," *IETE Journal of Research*, pp. 1-7, 2019.
- [8] I. Gassoumi, L. Touil, and A. Mtibaa, "An efficient QCA-Based full adder design with Power dissipation analysis," *International Journal of Electronics Letters*, no. just-accepted, 2022.
- [9] I. L. Bajec, N. Zimic, and M. Mraz, "The ternary quantum-dot cell and ternary logic," *Nanotechnology*, vol. 17, no. 8, p. 1937, 2006.
- [10] L. Wang and G. Xie, "A novel XOR/XNOR structure for modular design of QCA circuits," *IEEE Transactions on Circuits and Systems II: Express Briefs*, vol. 67, no. 12, pp. 3327-3331, 2020.
- [11] R. Singh and D. K. Sharma, "Design of efficient multilayer RAM cell in QCA framework," *Circuit World*, 2020.
- [12] D. Mukhopadhyay and P. Dutta, "A study on energy optimized 4 dot 2 electron two dimensional quantum dot cellular automata logical reversible flip-flops," *Microelectronics Journal*, vol. 46, no. 6, pp. 519-530, 2015.
- [13] F. S. Torres, R. Wille, P. Niemann, and R. Drechsler, "An energy-aware model for the logic synthesis of quantum-dot cellular automata," *IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems*, vol. 37, no. 12, pp. 3031-3041, 2018.
- [14] B. Das, M. Mahmood, M. Rabeya, and R. Bardhan, "An effective design of 2: 1 multiplexer and 1: 2 demultiplexer using 3-dot QCA architecture," in *2019 International Conference on Robotics, Electrical and signal Processing Techniques (ICREST)*, 2019: IEEE, pp. 570-575..
- [15] M. Ghosh, D. Mukhopadhyay, and P. Dutta, "Design of an arithmetic circuit using non-reversible adders in 2 dot 1 electron QCA," *Microsystem Technologies*, vol. 25, no. 5, pp. 1719-1729, 2019.
- [16] A. Sarker, M. B. A. Miah, and A. N. Bahar, "Design of 1-bit comparator using 2 dot 1 electron quantum-dot cellular automata," *International Journal of Advanced Computer Science and Applications*, vol. 8, no. 3, pp. 481-485, 2017.
- [17] M. Ghosh, D. Mukhopadhyay, and P. Dutta, "A 2 dot 1 electron quantum cellular automata based parallel memory," in *Information systems design and intelligent applications*: Springer, 2015, pp. 627-636.
- [18] M. Ghosh, D. Mukhopadhyay, and P. Dutta, "Energy efficient designing approach of flip-flops using 2-dot 1-electron QCA," in *International Conference on Computational Intelligence, Communications, and Business Analytics*, 2018: Springer, pp. 477-490.
- [19] K. Datta, D. Mukhopadhyay, and P. Dutta, "Comprehensive design and analysis of Gray code counters using 2-dimensional 2-dot 1-electron QCA," *Microsystem Technologies*, pp. 1-19, 2018.
- [20] R. Akbari-Hasanjani, R. Sabbaghi-Nadooshan, and M. R. Tanhayi, "New polarization and power calculations with error elimination in ternary QCA," *Computers & Electrical Engineering*, vol. 96, p. 107557, 2021.
- [21] M. A. Tehrani, S. Bahrami, and K. Navi, "A novel ternary quantum-dot cell for solving majority voter gate problem," *Applied Nanoscience*, vol. 4, no. 3, pp. 255-262, 2014.
- [22] Akbari-Hasanjani R, Sabbaghi-Nadooshan R (2021) Innovation Quinary and n-Value toward Fuzzy Logic QCA Cell Design. *Advanced Theory and Simulations*: 2100304
- [22] S. F. Kamali, S. Tabrizchi, S. Mohammadyan, M. Rastgoo, and K. Navi, "Designing positive, negative and standard gates for ternary logics using quantum dot cellular automata," *Computers & Electrical Engineering*, vol. 83, p. 106590, 2020.
- [23] K. Das, D. De, and M. De, "Modified ternary Karnaugh map and logic synthesis in ternary quantum dot cellular automata," *IETE Journal of Research*, vol. 62, no. 6, pp. 774-785, 2016.
- [24] P. Pain, A. Sadhu, K. Das, and M. R. Kanjilal, "Physical Proof and Simulation of Ternary Logic Gate in Ternary Quantum Dot Cellular Automata," in *Computational Advancement in Communication Circuits and Systems*: Springer, 2020, pp. 375-385.

- [25] A. Simonetta and M. C. Paoletti, "Designing Digital Circuits in Multi-Valued Logic," *International Journal on Advanced Science, Engineering and Information Technology*, vol. 8, no. 4, pp. 1166-1172, 2018.
- [26] Z. Toffano and F. Dubois, "Interpolating binary and multivalued logical quantum gates," in *Multidisciplinary Digital Publishing Institute Proceedings*, 2017, vol. 2, no. 4, p. 152.
- [27] R. Akbari-Hasanjani and R. Sabbaghi-Nadooshan, "Design and Simulation of Innovative QCA Quaternary Logic Gates," *Advanced Theory and Simulations*, vol. 4, no. 9, p. 2100069, 2021.
- [28] J. Timler and C. S. Lent, "Power gain and dissipation in quantum-dot cellular automata," *journal of applied physics*, vol. 91, no. 2, pp. 823-831, 2002.
- [29] P. D. Tougaw and C. S. Lent, "Dynamic behavior of quantum cellular automata," *Journal of Applied Physics*, vol. 80, no. 8, pp. 4722-4736, 1996.
- [30] S. Srivastava, S. Sarkar, and S. Bhanja, "Estimation of upper bound of power dissipation in QCA circuits," *IEEE transactions on nanotechnology*, vol. 8, no. 1, pp. 116-127, 2008.
- [31] S. Srivastava, A. Asthana, S. Bhanja, and S. Sarkar, "QCAPro-an error-power estimation tool for QCA circuit design," in *2011 IEEE international symposium of circuits and systems (ISCAS)*, 2011: IEEE, pp. 2377-2380.
- [32] W. Liu, S. Srivastava, L. Lu, M. O'Neill, and E. E. Swartzlander, "Are QCA cryptographic circuits resistant to power analysis attack?," *IEEE transactions on nanotechnology*, vol. 11, no. 6, pp. 1239-1251, 2012.
- [33] A. N. Bahar, F. Ahmad, S. Wani, S. Al-Nisa, and G. M. Bhat, "New modified-majority voter-based efficient QCA digital logic design," *International Journal of Electronics*, vol. 106, no. 3, pp. 333-348, 2019.
- [34] M. Macucci *et al.*, "A QCA cell in silicon-on-insulator technology: theory and experiment," *Superlattices and microstructures*, vol. 34, no. 3-6, pp. 205-211, 2003.
- [35] M. Hofmann *et al.*, "Mix-and-match lithography and cryogenic etching for NIL template fabrication," *Microelectronic Engineering*, vol. 224, p. 111234, 2020.
- [36] P. Gao *et al.*, "Plasmonic lithography for the fabrication of surface nanostructures with a feature size down to 9 nm," *Nanoscale*, vol. 12, no. 4, pp. 2415-2421, 2020.
- [37] W. Chen and H. Ahmed, "Fabrication of 5-7 nm wide etched lines in silicon using 100 keV electron-beam lithography and polymethylmethacrylate resist," *Applied physics letters*, vol. 62, no. 13, pp. 1499-1501, 1993.
- [38] B. Päivänranta, A. Langner, E. Kirk, C. David, and Y. Ekinici, "Sub-10 nm patterning using EUV interference lithography," *Nanotechnology*, vol. 22, no. 37, p. 375302, 2011.
- [39] M. B. Haider, J. L. Pitters, G. A. DiLabio, L. Livadaru, J. Y. Mutus, and R. A. Wolkow, "Controlled coupling and occupation of silicon atomic quantum dots at room temperature," *Physical review letters*, vol. 102, no. 4, p. 046805, 2009.
- [40] M. B. Haider, J. L. Pitters, G. A. DiLabio, L. Livadaru, J. Y. Mutus, and R. A. Wolkow, "Controlled coupling and occupation of silicon atomic quantum dots at room temperature," *Physical review letters*, vol. 102, no. 4, p. 046805, 2009.
- [41] S. N. Chowdhury, A. Faiyaz, and K. M. Ishtiak, "Formulation and Design of Useful Logic Gates Using Quaternary Algebra."
- [42] I. Jahangir, D. M. N. Hasan, S. Islam, N. A. Siddique, and M. M. Hasan, "Development of a novel quaternary algebra with the design of some useful logic blocks," in *2009 12th International Conference on Computers and Information Technology*, 2009: IEEE, pp. 197-202.
- [43] N. K. Naware, D. S. Khurge, and S. Bhandari, "Review of Quaternary Algebra & its Logic Circuits," in *2015 International Conference on Computing Communication Control and Automation*, 2015: IEEE, pp. 969-973.
- [44] R. Akbari-Hasanjani and R. Sabbaghi-Nadooshan, "New Design of Binary to Ternary Converter," *IETE Journal of Research*, pp. 1-12, 2021.
- [45] S. A. Ebrahimi, M. R. Reshadinezhad, A. Bohlooli, and M. Shahsavari, "Efficient CNTFET-based design of quaternary logic gates and arithmetic circuits," *Microelectronics Journal*, vol. 53, pp. 156-166, 2016.
- [46] F. Sharifi, M. H. Moaiyeri, K. Navi, and N. Bagherzadeh, "Ultra-low-power carbon nanotube FET-based quaternary logic gates," *International Journal of Electronics*, vol. 103, no. 9, pp. 1524-1537, 2016.
- [47] R. C. G. da Silva, H. Boudinov, and L. Carro, "A novel voltage-mode CMOS quaternary logic design," *IEEE Transactions on Electron devices*, vol. 53, no. 6, pp. 1480-1483, 2006.



Reza Akbari-Hasanjani received B. Sc. and M.Sc. degree in electrical engineering from Central Tehran branch, Islamic Azad University, Tehran, Iran, in 2012 and 2015 and the Ph.D. degree in electrical engineering from Central Tehran branch, Islamic Azad University, Tehran, Iran in 2022. His research interests are nanocomputing, Reversible Gates, Multiple-value Logic and QCA.



Reza Sabbaghi-Nadooshan received the B.S. and M.S. degree in electrical engineering from the Iran University of Science and Technology, Tehran, Iran, in 1991 and 1994 and the Ph.D. degree in electrical engineering from the Science and Research Branch, Islamic Azad University, Tehran, Iran in 2010. Now he is Associate Professor of Department of Electronics in Central Tehran branch, Islamic Azad University, Tehran, Iran. He has published more than 100 research papers in various international journals and conferences. His current research interests include Multiple value logic, QCA, and nanocomputing. He is on the panel of reviewers for various international journals.



Majid Haghparast received his B.Sc. in computer engineering in 2003. He received his MSc. and Ph.D. degrees in computer in 2006 and 2009, respectively. Since 2007, he was affiliated with the Computer Engineering Faculty, IAU University, Tehran, where he was an associate professor. He has published more than 100 research papers in various international journals and conferences. From April 2017 to January 2018, he has conducted his sabbatical at the Johannes Kepler University, Linz, Austria, where he also was a Research Fellow. Dr. Haghparast is on the panel of reviewers for various international journals. He is an Associate Editor for the Springer Cluster Computing Journal. Since September 2021, Majid has been with the Faculty of Information Technology, University of Jyväskylä, Jyväskylä, Finland.