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Charge Transport Mechanisms in Heavy-Ion Driven Leakage Current in Silicon Carbide Schottky Power Diodes

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Abstract—Under heavy-ion exposure at sufficiently high reverse bias voltages silicon carbide (SiC) Schottky diodes are observed to exhibit gradual increases in leakage current with increasing ion fluence. Heavy-ion exposure alters the overall reverse current-voltage characteristics of these diodes, leaving the forward characteristics practically unchanged. This paper discusses the charge transport mechanisms in the heavy-ion damaged SiC Schottky diodes. A macro model, describing the reverse current-voltage characteristics in the degraded SiC Schottky diodes is proposed.

Index Terms—Current-voltage characteristics, Ion radiation effects, Modeling, Power semiconductor devices, Schottky diodes, Silicon carbide

I. INTRODUCTION

Silicon Carbide (SiC) devices have become attractive for power applications both in space and at ground level. Compared to silicon, SiC is a superior material for power devices with higher breakdown field and thermal conductivity. However, SiC power devices (MOSFETs and diodes) have been observed to be surprisingly vulnerable to particle radiation [1]–[5]. Moreover, SiC Schottky diodes exhibit responses to heavy ions that are not typically observed in silicon-based power devices. Above device-specific bias voltage levels, silicon-based power devices, such as power MOSFETs, typically experience directly catastrophic failure, either Single Event Gate Rupture (SEGR) or Single Event Burnout (SEB). However, Schottky devices, made of SiC [3], Si [6], and GaN [7]–[9], have been reported to exhibit gradual degradation under heavy-ion exposure. SiC Schottky devices also undergo catastrophic SEB when the bias level during irradiation is sufficiently high [1], [3].

This paper concentrates on the heavy-ion induced parametric degradation in SiC Schottky diodes. The gradual degradation observed in these devices makes it difficult to assess their radiation survivability, and to estimate their reliability in radiation environments. For this reason space agencies are cautious in implementing SiC devices in their space systems [4], [5]. Also at ground level, SiC power devices can exhibit radiation-induced failures due to cosmic rays [10], [11].

Heavy ion-induced leakage current degradation of a SiC Schottky diode modifies its entire reverse current-voltage characteristic, while no degradation is observed in the forward characteristics. This paper discusses the charge transport mechanisms (current-voltage characteristics) in the damaged SiC structure caused by heavy ions. It is found that the charge transport is governed by space charge-limited flow. A macro model is proposed, which can be used to describe the heavy-ion modified electrical characteristics of SiC Schottky diodes.

II. EXPERIMENTAL METHODS

The devices used in this study were commercial SiC Schottky power diodes manufactured by STMicroelectronics. The devices and their characteristics are given in Table 1. The active areas for the diodes have been estimated from micrographs. The ion beams and their characteristics are tabulated in Table 2. The diodes were decapsulated prior to the irradiation tests to enable the heavy ions to penetrate the sensitive layers in the device, i.e., the Schottky junction and the epitaxial layer.
The total thickness of overlaying passivation layers and the epitaxial layer was estimated to be of the order of ten micrometers. Hence, the projected ranges for the ions used in this work, as described in Table 2, are sufficient to penetrate the active region.

The heavy-ion irradiations for this work were performed at the RADiation Effects Facility (RADEF) in the Accelerator Laboratory of the University of Jyväskylä [12]. All the irradiations were performed at room temperature (RT) and in vacuum conditions and all the device characterization was done at room temperature. All the ion beams were at normal incidence to the device surface. The ion flux ranged between 20 and \(6 \cdot 10^5 \, \text{cm}^{-2} \cdot \text{s}^{-1}\) and cumulative fluences for each irradiation run varied from \(10^3\) to \(10^7 \, \text{cm}^{-2}\).

The current-voltage characteristics for the devices were measured both for the forward and the reverse voltages, before and after each irradiation run. During the irradiations the device under test (DUT) was biased at fixed reverse voltage and the leakage current was monitored. During heavy-ion tests the biasing and device characterization was done by using a Keithley 2636 SourceMeter [15]. The voltage limit for the 2636 unit is 200V, which limited the measurement voltages during the irradiations. All the data after annealing, both at room and at high temperature, were obtained by using a Keithley 2410 high-voltage SourceMeter [16]. The voltage limit for the 2410 unit is 1100V. Because the bare surface of the die was exposed, the voltage levels in the annealing studies were limited by sparking, which occurred approximately at \(-400\, \text{V}\).

### III. RESULTS

The heavy-ion exposure causes an increase in reverse leakage current in the diodes. This effect was first reported in [3]. The effect is illustrated in Figure 1, where the evolution of leakage current during Xe-ion exposure is given for several different biasing configurations for STPSC1006D diodes. The response in STPSC10H065DY diodes was similar. At higher reverse biases during exposure, the linear dependence of leakage current on ion fluence (\(\Phi\)) is clearly visible. The data in this plot were obtained for three different individual devices. The data for bias voltages between \(-100\, \text{V}\) and \(-150\, \text{V}\) were obtained from one device, and data for biases of \(-180\, \text{V}\) and \(-200\, \text{V}\) were obtained for two separate devices. In order for this gradual degradation to take place the bias voltage has to exceed a certain threshold, which in this case lies between \(-130\, \text{V}\) and \(-140\, \text{V}\).

![Figure 1. Leakage current evolution in STPSC1006D devices as a function of Xe ion fluence at different irradiation bias voltages.](image1)

This current was monitored throughout the exposure, so the measurement bias and irradiation bias are the same.

![Figure 2. Reverse current-voltage characteristics for a STPSC1006D diode before and after exposure to Xe ions at a bias of -200V. Room temperature (RT) annealed data was taken 3 months after the heavy-ion exposure.](image2)
1.7\text{V} forward bias, corresponding to 1.8\text{W} of power, to the diode for 24 hours. No observable annealing was found. (The actual junction temperature was not known for this biasing condition.)

The forward current-voltage curves for the same device as in Figure 2 are presented in Figure 3. Notice that the forward characteristics of these diodes were not affected significantly even at fluence levels as high as $10^7$ ions cm$^{-2}$.

These data are presented in Figure 4, where the data from Figure 2 are normalized with the corresponding cumulative fluence levels. Here, one can see that the entire current-voltage curve is linearly dependent on the fluence. For lower values of fluence the current values deviate from the rest of the dataset since the increase in leakage current is relatively small and the reverse current at low voltages does not rise above the noise floor.

The same effect in the current-voltage characteristics was observed when diodes were irradiated with Kr ions. Correspondingly, the Kr ion data, obtained at a reverse bias of 200 V, are presented in Figure 5. The other device type STPSC10H065DY exhibited very similar characteristics after heavy-ion exposure. The graphs in Figure 4 and Figure 5 also present data extracted from the proposed model, which is discussed below.

![Figure 3. Forward current-voltage characteristics at several exposure levels for a STPSC1006D diode irradiated with Xe ions at a bias of -200V.](image)

![Figure 4. Reverse current-voltage characteristics normalized with the cumulative ion fluence for STPSC1006D diode, irradiated by Xe ions with applied bias voltage of -200 V. The solid line represents the model described in Eq.(3). Dashed and dotted lines are determined by Eqs. (1) and (2), respectively.](image)

![Figure 5. Reverse current-voltage characteristics normalized with the cumulative ion fluence for a STPSC1006D diode irradiated with Kr ions with applied bias voltage of -200 V. The deviation of the lower fluence data from the rest at low voltages is partly due to the noise level in the current measurement, but also due to the smaller effect the Kr ions at this irradiation bias have on the leakage current.](image)
structures (shaping the voltage contours) is affected by the heavy ion exposure in this work. Moreover, the number of incremental steps observed in the leakage current at low ion fluxes correlates with the active diode area. All this suggests that the degradation occurs in the active region of the diode.

In [18] it is discussed that the heat spike, induced by the heavy ion and the bias, can extend up to a few micrometers from the ion’s trajectory, but the possible material melting would occur only within some tens of nanometers from the trajectory. According to [18], the melting is considered to be the primary cause for the degradation. Hence, in the first order approximation, the heavy-ion induced leakage paths can be viewed as separate components parallel to the pristine Schottky structure. The proposed macro model is illustrated in Figure 6, where the ion-induced part is depicted as two voltage-controlled-current-sources (VCCS) in series, which is placed in parallel with a pristine or un-irradiated Schottky structure.

![Figure 6. Schematic layout of the heavy-ion induced VCCSs parallel to a pristine Schottky structure.](image)

The number of the ion-induced VCCSs is assumed to increase linearly with increasing ion exposure. In other words, on average, each ion hitting the device is assumed to create a leakage path, which is represented by these parallel VCCSs. It is assumed that the current in the pristine Schottky structure remains much lower compared to the damaged region induced by the ions. Hence, the reverse current is governed by the conduction set by the VCCSs.

The first VCCS is described by a power law

\[
\frac{I}{\Phi} = k_1 V_1^n, \tag{1}
\]

where \(k_1\) and \(n\) are fitting parameters. At higher reverse voltages the current deviates from this power-law. At these voltages the second VCCS starts to govern the current flow. This part is described by

\[
\frac{I}{\Phi} = k_2 V_2^{3/2}, \tag{2}
\]

where again \(k_2\) is a fitting parameter. Now, the overall reverse current in the diode, after an ion exposure, can be given as

\[
I = \Phi \cdot k_1 V_{BIAS} - \left( \frac{I}{\Phi} \frac{k_1}{k_2} \right)^{2/3} n, \tag{3}
\]

where \(V_{BIAS} = V_1 + V_2\) is the total voltage applied across the diode.

It was found that for all the measured current-voltage data, for both device types, the parameter \(n\) has an average value of 8.2 ± 0.4. This value was used in the analysis to determine the rest of the fitting parameters, \(k_1\) and \(k_2\). In order to illustrate the applicability of the proposed model, Figure 4 and Figure 5 present the data derived from Eqs. (1), (2) and (3).

All the obtained parameters are given in Table 3. There are some differences in the fitting parameters for the two studied devices. These differences can be partly attributed to the difference in the device active area. Also, the different thickness of the epilayer, which is unknown for these devices, could cause differences in the parameter values as it determines the electric field in the SiC below the Schottky junction.

<table>
<thead>
<tr>
<th>ion</th>
<th>irrad. bias</th>
<th>(k_1) [A cm(^2) V(^{-n})]</th>
<th>(k_2) [A cm(^2) V(^{-3/2})]</th>
</tr>
</thead>
<tbody>
<tr>
<td>Xe</td>
<td>-200</td>
<td>2.3 (\times) 10(^{-29})</td>
<td>3.9 (\times) 10(^{-13})</td>
</tr>
<tr>
<td>Xe</td>
<td>-180</td>
<td>4.5 (\times) 10(^{-30})</td>
<td>2.2 (\times) 10(^{-13})</td>
</tr>
<tr>
<td>Xe</td>
<td>-160</td>
<td>7.6 (\times) 10(^{-31})</td>
<td>2.7 (\times) 10(^{-13})</td>
</tr>
<tr>
<td>Kr</td>
<td>-200</td>
<td>2.0 (\times) 10(^{-33})</td>
<td>6.8 (\times) 10(^{-16})</td>
</tr>
<tr>
<td>Xe</td>
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<td>2.9 (\times) 10(^{-29})</td>
<td>1.4 (\times) 10(^{-13})</td>
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<tr>
<td>Xe</td>
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<td>5.8 (\times) 10(^{-30})</td>
<td>1.1 (\times) 10(^{-13})</td>
</tr>
<tr>
<td>Kr</td>
<td>-200</td>
<td>3.3 (\times) 10(^{-33})</td>
<td>3.7 (\times) 10(^{-16})</td>
</tr>
</tbody>
</table>

### V. DISCUSSION

The power-law given by Eq.(1) strongly suggests that the current transport in the damaged region is due to space-charge-limited-conduction (SCLC), described in [19]. The value for the parameter \(n = 8.2\) determines the energy distribution of the traps, \(n_t\), in the SiC energy bandgap in the region damaged by the heavy ions. According to [19], when the traps are exponentially distributed in the bandgap as a function of energy, \(E\), measured from the bottom of the SiC conduction band (CB), it gives \(n_t \propto \exp(-E/kT_c)\). Thus, the SCLC will be described by \(I \propto V^{T_c+1}\). Here \(T_c\) is a characteristic temperature for the trap distribution and \(T\) is the ambient device temperature. High values of \(T_c\) would make the trap distribution slowly varying with energy. In this case, \(T_c = (m - 1) \cdot T = 7.2 \cdot 300K = 2160K\), which can be considered as a high value. The trap distribution in the damaged SiC Schottky junction is illustrated in Figure 7. In
this graph the color gradient represents the exponentially varying trap distribution in the band gap of SiC. Trap densities are the highest near the CB, and exponentially decaying with energy below CB.

The expression in Eq. (2) corresponds to ballistic transport, which is also known as the Child-Langmuir law, or the three-halves law, which determines the maximum current in SCLC [20] in a scatter-free environment. The data clearly show that the leakage current starts to approach this limit at higher voltages.

![Band diagram for Schottky junction with traps](image_url)

**Figure 7.** Band diagram for Schottky junction with traps. Variation in trap density is illustrated by the color gradient. Darker color corresponds to higher trap density.

As seen in Figure 4 and Figure 5, this macro model provides a good description of the current-voltage characteristics of the Schottky diode after heavy-ion exposure. The schematic model, depicted in Figure 6, could be used in circuit simulations by applying Eqs. (1) and (2) in it, to estimate overall systems' response under ion exposure for space applications. The parameters $k_1$ and $k_2$, obtained for Eq.(3), are determined by the device characteristics, such as the size of the active diode area. Also the parameter $n$ can depend on characteristics such as the Schottky metal and the doping levels in the SiC.

VI. CONCLUSIONS

This work shows that the static electrical characteristics of SiC Schottky power diodes, which are degraded by heavy-ion exposure, can be modeled by using a simple macro model. The proposed model assumes the heavy-ion induced leakage paths to be governed by SCLC mechanisms. These damaged regions can be viewed as separate nonlinear components parallel to the undisturbed SiC Schottky diode structure. The exact parameterization of the model depends on the characteristics of the individual device type. Hence, device characterization by using ground testing with heavy ions is required in order to utilize this model.

Moreover, due to high current requirements, SiC Schottky power devices can have relatively thick bonding wires shadowing the die. At beam energies, used in typical ground testing, scattering from the bonding wires may play a role in the device response. Hence, in order to get a more detailed picture of heavy-ion induced damage in SiC Schottky power diodes one would need to consider also the testing conditions as discussed in [21].

The current data do not show any signs of latent defects affecting the long-term reliability of heavy-ion exposed SiC Schottky diodes. However, these data are very limited and should be verified in dedicated experiments in order to fully conclude how the degradation affects the device reliability.

VII. ACKNOWLEDGMENT

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