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Optimization and performance investigation of 1-Toffoli gate quantum full adders for spin-torque-based n -qubit architecture

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Abstract

Quantum computing (QC) is suitable for reversible computing due to its inherent parallel processing ability and fast speed. It also helps to address the issue of high-power dissipation in classical computing. Moreover, QC gates are the sequence of elementary operations such as single-qubit rotation and two-qubit entanglement. Elementary quantum operations are required to be reduced for the realization of complex computing. In this paper, optimization of 1-Toffoli gate-based quantum full adders (QFAs) in terms of the number of elementary operations with the help of quantum library $\{R_y, R_z, \sqrt{\text{SWAP}}\}$ is carried out. Moreover, the performance of two different 1-Toffoli QFAs is investigated in terms of execution time, fidelity, and number of electrons required to realize the QFAs. Improvement in fidelity is 0.7% and 0.57% for QFA1 and QFA2, respectively, compared to the fidelity of 2-Toffoli QFA. A 9.97% increase in execution time is mandatory for the QFA2 compared to QFA1. The QFA2 takes 5% more number of electrons in comparison to QFA1.

Keywords Quantum computing · Quantum gates · Quantum circuit optimization · Reversible computing · Spin-torque

1 Introduction

The Semiconductor industry is struggling to address the next generations of complex computing problems that require a large number of transistors on a chip, thereby demanding huge transistor density supported by a few nanometers dimension of the transistor. As the growth continues with more and more transistors on a semiconductor chip, the heat dissipation issue is becoming increasingly difficult to handle. Bennet (Bennett 1982) discussed

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the thermodynamics of computation to explain the utilization of a reversible approach for reducing the power dissipation in the circuits. Due to the reversible approach, there is no loss of information as there is a one-to-one mapping of input to the output. Unlike irreversible circuits, in reversible circuits, the information about the inputs can be extracted from the information available at the output. One of the important ways to achieve reversible computing is through quantum computing (QC) due to its inherent ability to process data faster than conventional irreversible classical computing.

Along with QC (Nielsen and Chuang 2000; Saharia et al. 2019), artificial intelligence (AI) (Brachman and Henig 1988) and machine learning (ML) (Elfadel et al. 2019) are future areas in the Very Large Scale Integration (VLSI) domain. The design of QC algorithms and the quantum circuits for realizing reversible computing need extensive efforts compared to their realization based on classical transistor-based reversible computing. Moreover, it is very challenging to realize the quantum circuits for reversible computing physically. There are myriad ways to realize reversible computing physically, such as transistors (Bruce et al. 2002), Quantum Dot (QD) (Taylor et al. 2007), Superconducting (SC) (Strauch et al. 2003; DiCarlo et al. 2009), Ion Traps (IT) (Wineland et al. 1998), Rydberg atoms (Saffman and Walker 2005), Linear Photonics (LP) (Franson et al. 2004), Non-Linear Photonics (NP) (Munro et al. 2005), and Spin-Torque (ST) (Cordourier-Maruri et al. 2010; Sutton and Datta 2015; Kulkarni et al. 2018a; Sharma and Tulapurkar 2021; Bhat et al. 2022; Niknam et al. 2022; Selberherr and Sverdlov 2022; Komineas 2023; Glavin et al. 2022; Choi et al. 2022).

Quantum full adder (QFA) is one of the building blocks of reversible Boolean computing. The QFA is different from the conventional full adder in terms of the number of inputs and outputs (Peres 1985; Swaminathan et al. 1990; Maimistov 1009; D. V., Alexis, B. Desoete, A. Adamski, P. Pietrzak, M. Sibiński, and T. Widerski. 2000; Cheng and Tseng 2002; Thapliyal and Srinivas 2005; Navi et al. 2010; Qi et al. 2012; Abedi et al. 2015; Seyedi and Navimipour 2018, 2021; Doshanlou et al. 2022; Lanthaler et al. 2023). Therefore, it is necessary to design and implement the QFA efficiently to reduce the complexity of reversible Boolean computing. In this paper, two different 1-Toffoli gate-based QFAs are decomposed, optimized, and simulated for the spin-torque-based qubit architecture. The contribution is to reduce the number of elementary operations to improve the fidelity of the 1-Toffoli gate-based QFAs compared to the 2-Toffoli gate-based QFAs presented in the literature.

This paper consists of five sections, including the current introductory section. The various QFA designs are highlighted in the section II. Moreover, the conventional, reduced, and optimal decompositions of 1-Toffoli gate-based QFAs are presented in section II. Section III presents spin-torque-based n -qubit architecture. Post-optimization of 1-Toffoli QFAs' performance investigation in terms of fidelity, execution time, and the number of electrons required for the realization, is presented in section IV. Finally, section V concludes with the comparison of 1-Toffoli gate-based QFAs.

2 Optimization of quantum full adders

2.1 State of the art

There are various QC-based QFAs available in the literature. In (Cheng and Tseng 2002), the 1-bit quantum full adder is designed according to the classical truth table and utilized to

realize the n -bit adder. A recursive method of hand synthesis of the reversible quaternary full-adder circuit is proposed in Khan (2008). Quantum realization of a ternary full-adder using macro-level ternary Feynman and Toffoli gates is presented in Khan and Perkowski (2007). Muthukrishnan-Stroud, Feynman, Toffoli, and C2 NOT gates are used to design QFA (Asadi et al. 2020). Moreover, 2-Toffoli gates-based QFA design is presented in Golubitsky and Maslov (2012). However, these QFA designs have high quantum cost for their realization with the various physical machine descriptions. For the physical realization of the QFA, the quantum costs must be minimal for a particular physical machine description.

2.2 1-Toffoli QFAs

Recently, 1-Toffoli gate-based QFA1 (Pujar et al. 2019) and QFA2 (Gayathri et al. 2021) with minimum cost were designed. QFA1 is designed with three controlled-NOT (CNOT) gates and one Fredkin gate and is implemented with 40 transistors. However, it is not feasible to utilize this implementation in the era of miniaturization when the 40 transistors-based design is used as a building block for complex reversible computing implementation. Moreover, though the QFA1 design restores the reversibility, the two unutilized garbage outputs make the design of complex computing challenging, wherein QFA is a building block. Also, the 40 transistors-based implementation of reversible adder lacks reconfigurability. The reconfigurable spin-qubit architectures of the order of nanometers are discussed in Sutton and Datta (2015); Kulkarni et al. 2018a; Sharma and Tulapurkar 2020). The optimal decomposition of the Toffoli gate is utilized to implement 2-Toffoli gates-based QFA in our previous work (Kulkarni et al. 2018b). It is always feasible to realize the quantum circuits based reversible circuits on a quantum platform than a classical platform due to its ability to provide high speed and parallel processing. Moreover, as no physical machine description supports the direct implementation of the Fredkin gate, it needs to be decomposed further. In (Lin et al. 2013), the decomposition of the Fredkin gate with the two CNOT gates and one Toffoli gate, is presented.

The QFA1 is modified in this paper with the help of optimal decomposition of the Toffoli gate presented in Kulkarni et al. 2018b (see Fig. 1) for the further decomposition with the help of quantum library $\{Ry, Rz, \sqrt{SWAP}\}$ for the spin-torque-based n -qubit architecture. After modification with the help of the Toffoli gate, the QFA1 has five CNOT gates and one Toffoli gate. In this paper, the QFA1 is presented as a building block for the 4-bit reversible ripple carry adder (see Fig. 2). Unlike the design presented in Pujar et al. (2019), the QFA2 design (Gayathri et al. 2021) does not produce any garbage output and has inputs ‘A’ and ‘B’ directly available at the output (see Fig. 3). However, the design has one extra CNOT gate in comparison to the modified design of QFA1, as shown in Fig. 3. As reported in Lin et al. (2013), no physical machine description other than LP and NP offers direct implementation of the CNOT gate. Due to this, the decomposition of the extra CNOT gate further adds to the quantum cost of the QFA2 for other physical machine descriptions, including ST, at the elementary level.

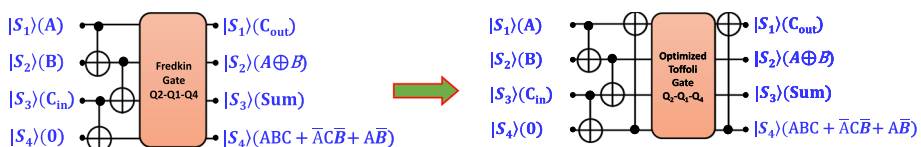


Fig. 1 Modification of QFA1 for the spin-torque-based n -qubit architecture

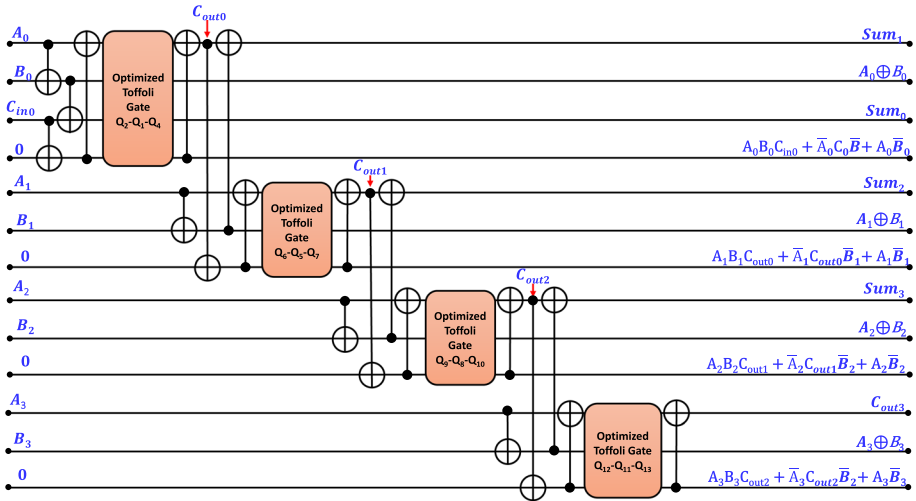
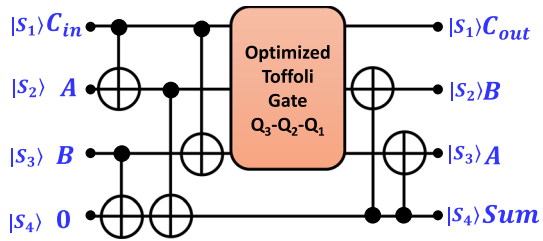


Fig. 2 4-bit reversible ripple carry adder-based on QFA1

Fig. 3 Modification of QFA2 for the spin-torque-based n-qubit architecture



It is essential to consider the physical realization at the elementary level to design quantum circuits. Moreover, QFA2 design is utilized to design the 10-qubit (3-bit) based ripple carry adder before its performance investigation with the help of any of the physical realizations (Bruce et al. 2002; Taylor et al. 2007; Strauch et al. 2003; DiCarlo et al. 2009; Wineland et al. 1998; Saffman and Walker 2005; Franson et al. 2004; Munro et al. 2005; Cordourier-Maruri et al. 2010; Sutton and Datta 2015). Therefore, in this paper, the optimal decomposition of the Toffoli gate (Shende and Markov 2009) is utilized for the performance investigation of QFA1 and QFA2 for the spin-torque-based n -qubit architecture. The number of operations required for the elementary quantum gates (Kulkarni et al. 2018a) are given in Table 1. In our earlier work (Kulkarni et al. 2018a), we had utilized the quantum library $\{R_y, R_z, \sqrt{SWAP}\}$ to reduce the number of operations by ($\sim 36\%$) i.e. number of operations to realize the universal gate CNOT from 11 to 7. As far as the spin-torque based qubit-architecture is concerned, it requires same procedure to carry out the two-qubit entanglement operation in terms of required number of electrons other than the gate barriers. Therefore, the two-qubit entanglement is considered as part of the quantum cost along with the single qubit rotations.

Table 1 Elementary gates operations (Kulkarni et al. 2018a)

Gate	Elementary operations		Reduction (%)
	Conventional	Reduced	
CNOT	11	7	36.36
SWAP	33	21	36.36
Toffoli	79	51	35.44
Fredkin	101	65	35.44

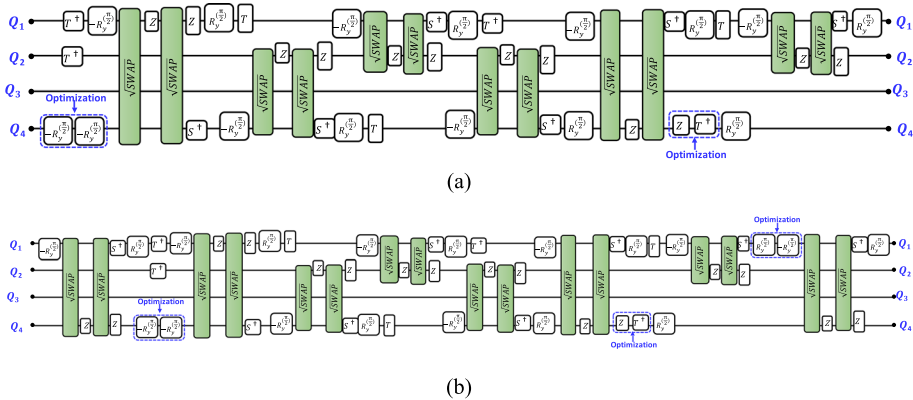


Fig. 4 Optimized decomposition of **a** Toffoli **b** Fredkin gates for QFA1

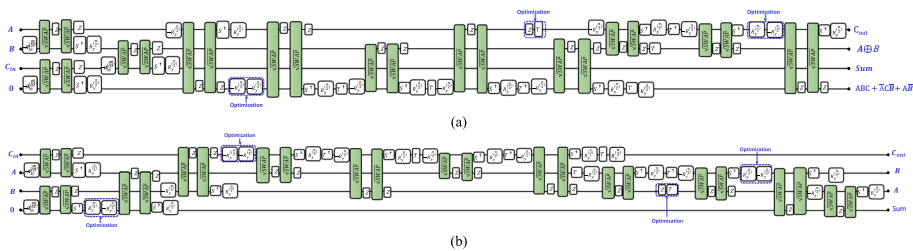


Fig. 5 Optimal decomposition of **a** QFA1 **b** QFA2

The optimized decomposition of the Toffoli gate and Fredkin gate utilized for QFA1 are presented in Fig. 4a, b, respectively. The single-qubit and two-qubit rotations-based optimal decompositions with the help of elementary quantum rotations library $\{R_y, R_z, \sqrt{SWAP}\}$ (Glavin et al. 2022) of 1-qubit Toffoli gate-based QFA1 and QFA2 are presented in Fig. 5a, b. The optimization at the elementary level is to merge the single qubit rotations in sequence with the same axis of rotation. Moreover, optimization helps to remove the redundant operations. The optimization rules $\begin{bmatrix} R_y^{(\pi/2)} \\ R_y^{(\pi/2)} \end{bmatrix} \Rightarrow R_y^{(\pi)}$, $\begin{bmatrix} Z \\ T^\dagger \end{bmatrix} \Rightarrow \begin{bmatrix} \sqrt{\pi} \\ R_x \end{bmatrix}$, and $\begin{bmatrix} R_y^{(\pi/2)} \\ R_y^{(\pi/2)} \end{bmatrix} \Rightarrow R_y^{(0)}$ are utilized to achieve the optimization process. The optimized version of the QFA is obtained from the conventional and reduced decompositions in our previous

work (Kulkarni et al. 2018b). Moreover, authors have proved in their previous work (Kulkarni et al. 2018b) that the optimal decomposition always provide improved fidelity and reduced execution time in comparison to their conventional and reduced counterparts. The execution time is based on the simulation of the quantum gates.

The elementary level optimization concerning their reduced counterparts is 5.81% and 7.69% for the QFA1 and QFA2, respectively, and is mainly based on the design of the quantum circuits for the QFAs.

3 Reconfigurable ST-based n -qubit architecture

The ST qubit architecture, as shown in Fig. 6, is conversed in Sutton and Datta (2015), and further, the architecture is discussed for y -axis directed spin-polarized electrons-based quantum circuits for W state (Sharma and Tulapurkar 2021) and CNOT gate (Kulkarni et al. 2018a). It is a reconfigurable architecture wherein moving electrons interact with the static qubits to rotate the qubit at a specific angle. The optimal QFA is suitable for the spin-torque based qubit-architecture only. It is due the decomposition of the universal CNOT gate utilized for the optimal QFA, is based on the spin-torque based qubit-architecture. The QFA1 will suit to any other architecture if the decomposition of the CNOT gate particularly the entanglement operation matches the entanglement operation for spin-torque based qubit architecture.

The transmission coefficient matrix (Kulkarni et al. 2018a) given in equation (1) is to investigate the performance of quantum gates. The second-order transmission coefficient matrix is:

$$t^{(2)} = \frac{1}{(1 + i4\Omega l)I + i\tilde{S}(\Omega - (i4\Omega^2 l (e^{i2kx_0} - 1)))} \tag{1}$$

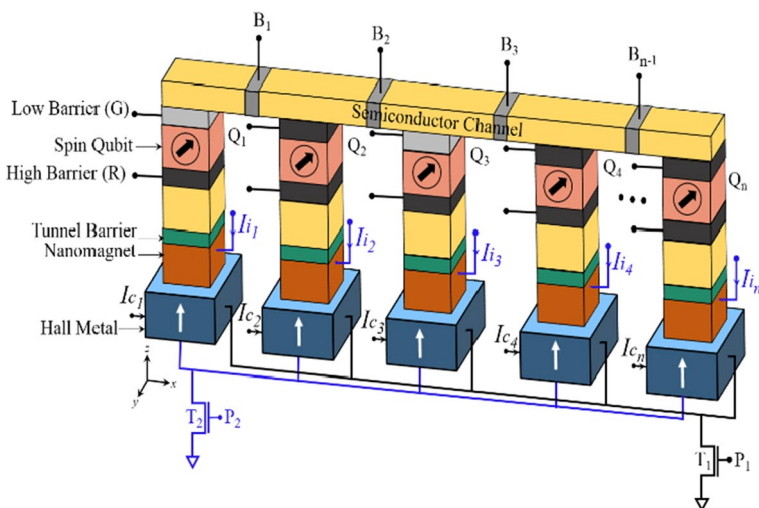


Fig. 6 Spin-torque-based n -qubit reconfigurable architecture

, where, \bar{S} is the standard basis matrix, $l = \frac{\Gamma_{Rfl}}{J}$, $\Omega = J/\hbar v$, and v is the velocity of the electron, Γ_{Rfl} is the height of the reflection barrier G , J is the hyperfine or exchange interaction, and x_0 is the distance of the qubit from the barrier, and \hbar is reduced Plank's constant.

The reflection matrix is

$$r^{(2)} = t^{(2)} - I \tag{2}$$

, where I is the identity matrix.

The generalized reflection matrix $R_F^{(2)}$ based on Eqs. (1) and (2) is provided in Eq. (3) as follows:

$$R_F^{(2)} = r^{(2)} - e^{i2kx_0} t^{(2)} [I_{2^{n+1}} + e^{i2kx_0} I_{2^{n+1}} R_0]^{-1} I_{2^{n+1}} t^{(2)} \tag{3}$$

, where, R_0 matrix represents the reflection barrier, and I is the identity matrix for the n -qubit architecture.

The reflection matrix for the two-qubit interaction on qubits Q_1 and Q_2 is given in equations (4) and (5), respectively, and the overall reflection matrix at the injection side barrier is given in equation (6).

$$R_{F_1}^{(2)} = r_1^{(2)} - e^{i2kx_0} t_1^{(2)} [I_{2^{n+1}} + e^{i2kx_0} I_{2^{n+1}} R_0]^{-1} I_{2^{n+1}} t_1^{(2)} \tag{4}$$

$$R_{F_2}^{(2)} = r_2^{(2)} - e^{i2kx_0} t_2^{(2)} [I_{2^{n+1}} + e^{i2kx_0} I_{2^{n+1}} R_{F_1}^{(2)}]^{-1} I_{2^{n+1}} t_2^{(2)} \tag{5}$$

$$R_{F_b}^{(2)} = r_b^{(2)} - e^{i2kx_{12}} t_b^{(2)} [I_{2^{n+1}} + e^{i2kx_{12}} I_{2^{n+1}} R_{F_2}^{(2)}]^{-1} I_{2^{n+1}} t_b^{(2)} \tag{6}$$

The subscripts 1, 2, and $^{(2)}$ represent qubits Q_1 , Q_2 , and second order matrix, respectively.

4 Performance investigation of 1-Toffoli gate-based QFAs

The performance of 1-Toffoli gate-based QFAs is investigated through simulations in terms of execution time, fidelity, and the number of electrons required to realize the entire 1-Toffoli gate-based QFA1 and QFA2. The spin-state evolution of four qubits Q_1 - Q_4 for the realization of QFA1 and QFA2 is shown in Fig. 7a, b, respectively.

The fidelities for optimal decomposition of the QFAs are given in Table 2. The improvement in fidelity is 0.7% and 0.57% for QFA1 and QFA2, respectively, over the fidelity of 2-Toffoli QFA. A 9.97% increase in execution time is required for QFA2 compared to QFA1. QFA2 takes 5% more number of electrons in comparison to QFA1. Therefore, QFA1 has better fidelity compared to QFA2. Moreover, QFA2 requires more execution time than QFA1 due to one extra CNOT gate. The quantum cost for the conventional, reduced, and optimal decompositions are presented in Fig. 8.

The quantum cost as defined for QFA1 before the optimization and after the optimization is 86 and 81, respectively. Similarly, the quantum cost for the QFA2 before and after the optimization is 91 and 84. The quantum gates state evolution from the initial state to

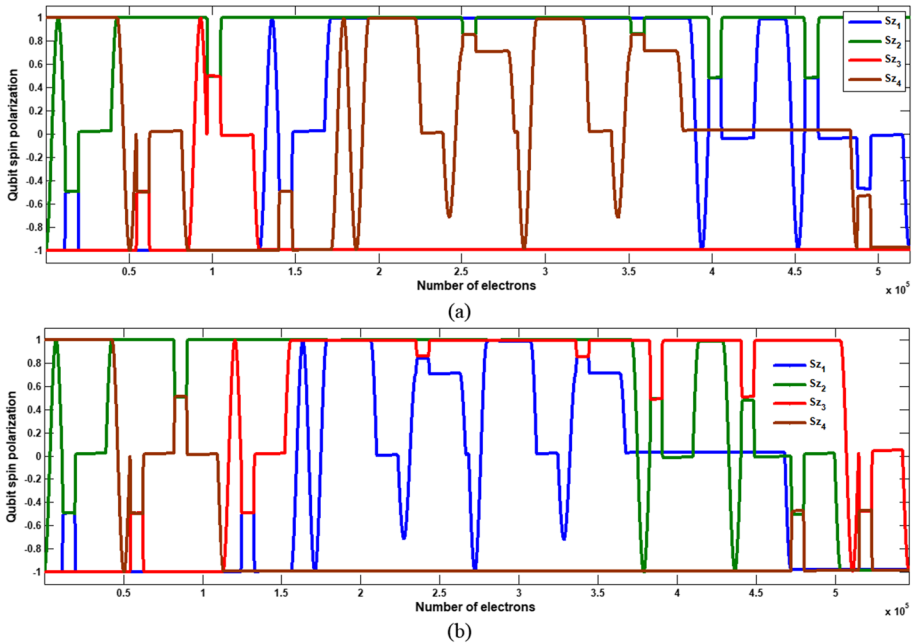


Fig. 7 **a** Spin qubit evolution for the input state $|1110\rangle$ (QFA1) **b** Spin qubit evolution for the input state $|1110\rangle$ (QFA2)

Table 2 Fidelity comparison (For second-order optimal)

Input state $ ABC_{in}\rangle$	1-Toffoli gate based QFA1 Fidelity (%)	1-Toffoli gate based QFA2 Fidelity (%)	2-Toffoli gate based QFA average Fidelity (%)
$ 000\rangle$	98.93	98.58	
$ 001\rangle$	98.51	97.79	
$ 010\rangle$	98.05	97.78	
$ 011\rangle$	98.10	98.37	97.65
$ 100\rangle$	97.92	98.18	
$ 101\rangle$	98.14	98.44	
$ 110\rangle$	98.44	98.05	
$ 111\rangle$	98.69	98.52	

final state is represented by the density matrix. The density matrix has two components i.e. Real and Imaginary (Imag).

A density matrix is defined as a 2×2 matrix, which is a linear combination of the unit/identity matrix and Pauli matrices σ_x , σ_y , and σ_z as

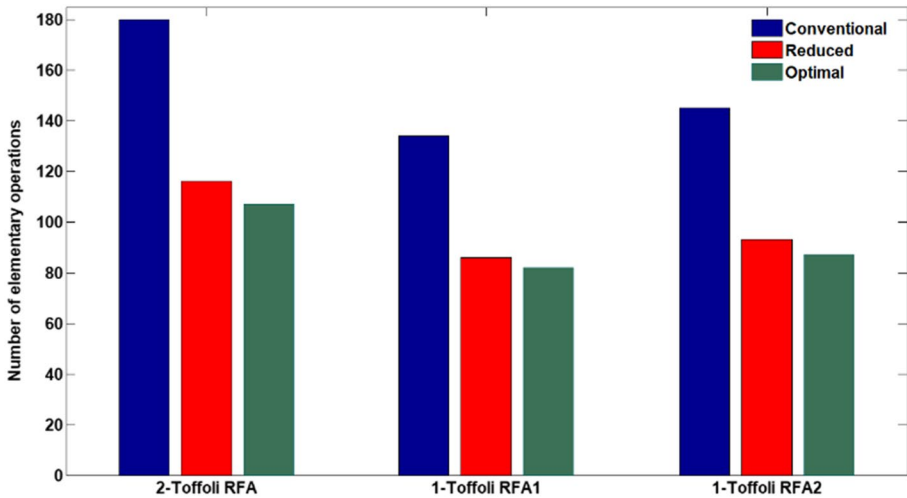


Fig. 8 Number of elementary operations for conventional, reduced, and optimal decomposition of QFAs

$$\rho = \frac{1}{2}(I + \vec{a}\vec{\sigma}) \tag{7}$$

The coefficient \vec{a} is known as the Bloch vector and is equal to

$$\vec{a} = Tr(\rho\vec{\sigma}) \tag{8}$$

The density matrix evolution for the input state $|1110\rangle$ and output state for QFA1 and QFA2 is presented in Fig. 9.

5 Conclusion

In this work, the optimization at elementary quantum circuits for 1-Toffoli gate-based QFAs and their realization on ST-based qubit architecture are presented. The achieved optimization is 5.81% and 7.69% for the QFA1 and QFA2, respectively, compared to their reduced counterparts. Moreover, the average fidelities for QFA1 and QFA2 are ~98.34% and ~98.21%, respectively. The improvement in fidelity is 0.7% and 0.57% for QFA1 and QFA2, respectively, over the fidelity of 2-Toffoli QFA. The execution time and the number of electrons required to realize QFA2 are more than QFA1 due to one extra CNOT gate.

Appendix 1

Parameters used to perform the simulations are taken from Sutton and Datta (2015).

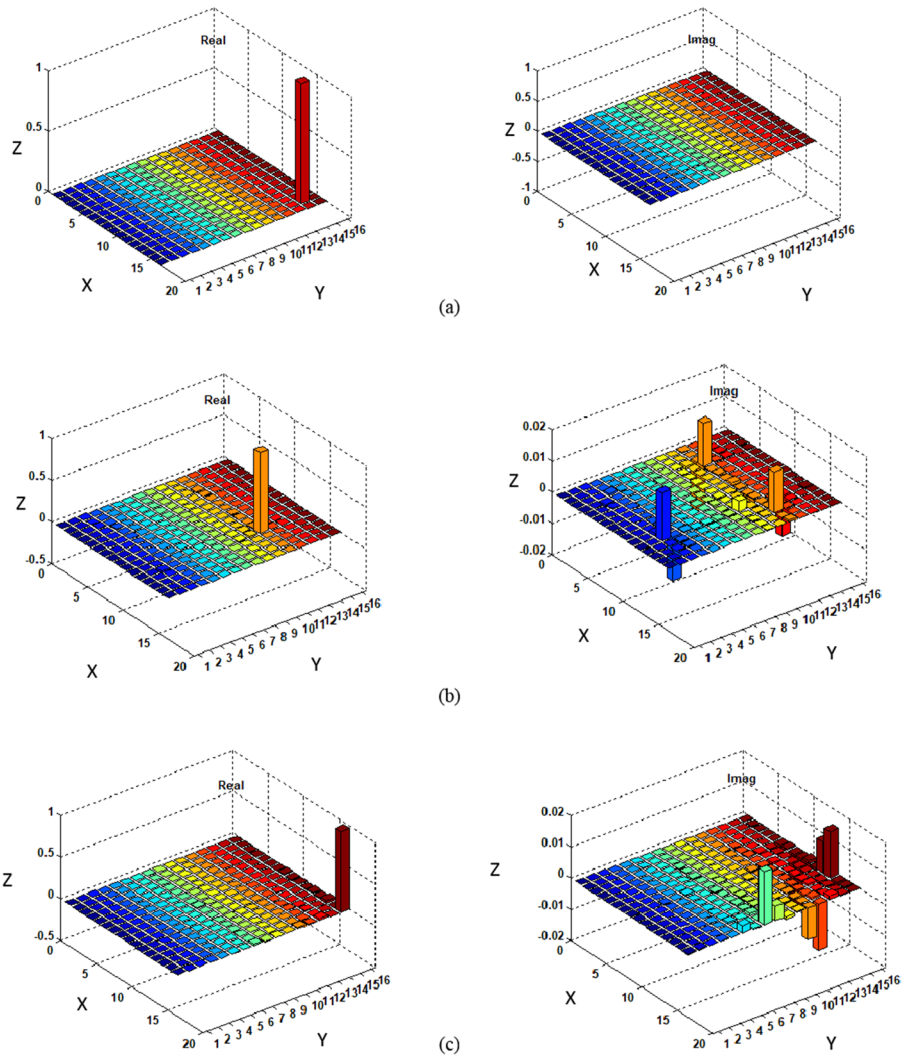


Fig. 9 a Input density matrix for the state $|1110\rangle$ b Output density matrix for the input state $|1110\rangle$ (QFA1) c Output density matrix for the input state $|1110\rangle$ (QFA2)

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Availability of data and materials Data sharing does not apply to this article as no new data were created or analyzed in this study.

Declarations

Conflict of interest The authors declare no conflicts of interest.

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References

- Abedi, D., Jaberipur, G., Sangsefidi, M.: Coplanar full adder in quantum-dot cellular automata via clock-zone-based crossover. *IEEE Trans. Nanotechnol.* **14**(3), 497–504 (2015)
- Asadi, M.A., Mosleh, M., Haghparast, M.: An efficient design of reversible ternary full-adder/full-subtractor with low quantum cost. *Quant. Inf. Process* **19**(7), 1–21 (2020)
- Alexis, D.V., Desoete, B., Adamski, A., Pietrzak, P., Sibiński, M., Widerski, T.: Design of reversible logic circuits by means of control gates. In: Integrated circuit design: power and timing modeling, optimization and simulation 10th international workshop, patmos 2000 göttingen, germany, september 13–15, 2000 Proceedings 10, pp. 255–264. Springer Berlin Heidelberg, (2000)
- Bennett, C.H.: The Thermodynamics of computation—a review. *Int. J. Theor. Phys.* **21**(12), 905–940 (1982)
- Bhat, H.A., Malik, G.F.A., Khanday, F.A.: Design and modelling of silicon quantum dot based single qubit spin quantum gates. *Int. J. Theor. Phys.* **61**, 258 (2022)
- Brachman, R.J., Henig, F.H.: The emergence of artificial intelligence technology. *At&t Tech. J.* **67**(1), 3–6 (1988)
- Bruce, J., Thornton, M.A., Shivakumaraiah, L., Kokate, P., Li, X.: Efficient adder circuits based on a conservative reversible logic gate. In: Proceedings IEEE computer society annual symposium on VLSI, Pittsburgh, PA, USA, pp. 25–26 (2002)
- Cheng, K.-W., Tseng, C.-C.: Quantum full adder and subtractor. *Electron. Lett.* **38**(22), 1343–1344 (2002)
- Choi, E.M., Sim, K.I., Burch, K.S., Lee, Y.H.: Emergent multifunctional magnetic proximity in van der waals layered heterostructures. *Adv. Sci.* **9**(21), 2200186 (2022)
- Courdourier-Maruri, G., Ciccarello, F., Omar, Y., Zarcone, M., de Coss, R., Bose, S.: Implementing quantum gates through scattering between a static and a flying qubit. *Phys. Rev. A* **82**(5), 052313 (2010)
- DiCarlo, L., Chow, J.M., Gambetta, J.M., Bishop, L.S., Johnson, B.R., Schuster, D.I., Majer, J., Blais, A., Frunzio, L., Girvin, S.M., Schoelkopf, R.J.: Demonstration of two-qubit algorithms with a superconducting quantum processor. *Nature* **460**(7252), 240–244 (2009)
- Elfadel, V.I.M., Boning, D.S., Li, X.: Machine learning in VLSI computer-aided design. Springer, Springer Nature Switzerland (2019)
- Franson, J.D., Jacobs, B.C., Pittman, T.B.: Quantum computing using single photons and the Zeno effect. *Phys. Rev. A* **70**(6), 062302–062313 (2004)
- Gayathri, S.S., Kumar, R., Dhanalakshmi, S., Kaushik, B.K., Haghparast, M.: T-Count optimized Wallace tree integer multiplier for quantum computing. *Int. J. Theor. Phys.* **60**(113), 2823–2835 (2021)
- Glavin, N.R., Ajayan, P.M., Kar, S.: Quantum Materials Manufacturing. *Adv. Mater.* **35**(27), 2109892 (2022)
- Golubitsky, O., Maslov, D.: A study of optimal 4-bit reversible Toffoli circuits and their synthesis. *IEEE Trans. Comput.* **61**(9), 1341–1353 (2012)
- Khan, M.H.A.: A recursive method for synthesizing quantum/reversible quaternary parallel adder/subtractor with look-ahead carry. *J. Syst. Architect.* **54**(12), 1113–1121 (2008)

- Khan, M.H.A., Perkowski, M.A.: Quantum ternary parallel adder/subtractor with partially-look-ahead carry. *J. Syst. Architect.* **53**(7), 453–464 (2007)
- Komineas, S.: Non-Hermitian dynamics for a two-spin system with PT symmetry. *Phys. Rev. B* **107**(9), 094435 (2023)
- Kulkarni, A., Prajapati, S., Kaushik, B.K.: Transmission coefficient matrix modeling of spin-torque based n -qubit architecture. *IEEE Trans. Very Large-Scale Integr. (VLSI) Syst.* **26**(8), 1461–1470 (2018a)
- Kulkarni, A., Prajapati, S., Verma, S., Kaushik, B.K.: Optimal Boolean logic quantum circuits decomposition for spin-torque based n -qubit architecture. *IEEE Trans. Magn.* **54**(10), 4100109 (2018b)
- Lanthaler, M., Niehoff, B.E., Lechner, W.: Scalable set of reversible parity gates for integer factorization. *Commun. Phys.* **6**(1), 73 (2023)
- Lin, C.C., Chakrabarti, A., Jha, N.K.: Optimized quantum gate library for various physical machine descriptions. *IEEE Trans. Very Large Scale Integr (VLSI) Syst.* **21**(11), 2055–2068 (2013)
- Maimistov, Andrei, I.: Reversible logic elements as a new field of application of optical solitons. *Quantum Electron* **25**(10), 1009 (1995)
- Munro, W.J., Nemoto, K., Spiller, T.P., Barrett, S.D., Kok, P., Beausoleil, R.G.: Efficient optical quantum information processing. *J. Opt. B* **7**(7), S135–S140 (2005)
- Navi, Keivan, R., Farazkish, S., Sayedsalehi, Rahimi Azghadi, M.: A new quantum-dot cellular automata full-adder. *Microelectron. J.* **41**(12), 820–826 (2010)
- Nielsen, M.A., Chuang, I.L.: Quantum computation and quantum information. Cambridge University Press, Cambridge (2000)
- Niknam, M., Chowdhury, M.F.F., Rajib, M.M., Misba, W.A., Schwartz, R.N., Wang, K.L., Bouchard, L.S.: Quantum control of spin qubits using nanomagnets. *Commun. Phys.* **5**(1), 284 (2022)
- Norouzi Doshanlou, A., Haghparast, M., Hosseinzadeh, M.: Novel quaternary quantum reversible half adder and full adder circuits. *IETE J. Res.* **68**(2), 1525–1531 (2022)
- Peres, A.: Reversible logic and quantum computers. *Phys. Rev. A* **32**(6), 3266 (1985)
- Pujar, J., Raveendran, S., Panigrahi, T., Vasantha, M.H., Kumar, Y.B.N.: Design and analysis of energy efficient reversible logic based full adder. In: IEEE 62nd international midwest symposium on circuits and systems (MWSCAS), Dallas, TX, USA, 4–7 (2019)
- Qi, X., Chen, F., Zuo, K., Guo, L., Luo, Y., Min, Hu.: Design of fast fault tolerant reversible signed multiplier. *Int. J. Phys. Sci.* **7**(17), 2506–2514 (2012)
- Saffman, M., Walker, T.G.: Analysis of a quantum logic device based on dipole-dipole interactions of optically trapped Rydberg atoms. *Phys. Rev. A* **72**(2), 022347–022421 (2005)
- Saharia, A., Maddila, R.K., Ali, J.: An elementary optical logic circuit for quantum computing: a review. *Opt. Quantum Electron.* **51**, 224 (2019)
- Selberherr, S., Sverdlov, V.: About electron transport and spin control in semiconductor devices. *Solid-State Electron.* **197**, 108443 (2022)
- Seyedi, S., Navimipour, N.J.: An optimized design of full adder based on nanoscale quantum-dot cellular automata. *Optik* **1**(158), 243–256 (2018)
- Seyedi, S., Navimipour, N.J.: Designing a three-level full-adder based on nano-scale quantum dot cellular automata. *Photonic Netw Commun.* **42**, 184–193 (2021)
- Sharma, A., Tulapurkar, A.A.: Generation of n -qubit W states using Spin Torque. *Phys. Rev. A* **101**(6), 062330–062411 (2020)
- Sharma, A., Tulapurkar, A.A.: Transmission-based tomography for spin qubits. *Phys. Rev. A* **103**(5), 052430 (2021)
- Shende, V.V., Markov, I.L.: On the CNOT-cost of TOFFOLI gates. *Quantum Inf. Comput.* **9**(5), 461–486 (2009)
- Strauch, F.W., Johnson, P.R., Dragt, A.J., Lobb, C.J., Anderson, J.R., Wellstood, F.C.: Quantum logic gates for coupled superconducting phase qubits. *Phys. Rev. Lett.* **91**(16), 167005–167014 (2003)
- Sutton, B., Datta, S.: Manipulating quantum information with spin torque. *Nature Scientific Report* **5**, 17912 (2015)
- Swaminathan, Gnanasekaran, J.H., Aylor, Johnson, B.W.: Concurrent testing of VLSI circuits using conservative logic. In: Proceedings., 1990 IEEE International Conference on Computer Design: VLSI in Computers and Processors, IEEE, pp. 60–65, (1990)
- Taylor, J.M., Petta, J.R., Johnson, A.C., Yacoby, A., Marcus, C.M., Lukin, M.D.: Relaxation, dephasing, and quantum control of electron spins in double quantum dots. *Phys. Rev. B* **76**(3), 035315–035317 (2007)
- Thapliyal, H., Srinivas. M.B.: Novel design and reversible logic synthesis of multiplexer based full adder and multipliers. In: 48th Midwest symposium on circuits and systems, IEEE, pp. 1593–1596 (2005)

Wineland, D.J., Monroe, C., Itano, W.M., Leibfried, D., King, B.E., Meekhof, D.M.: Experimental issues in coherent quantum-state manipulation of trapped atomic ions. *J. Res. Nat. Inst. Stand. Technol.* **103**(3), 259 (1998)

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