

# This is a self-archived version of an original article. This version may differ from the original in pagination and typographic details.

Author(s): Galloway, Kenneth F.; Witulski, Arthur F.; Schrimpf, Ronald D.; Sternberg, Andrew L.; Ball, Dennis R.; Javanainen, Arto; Reed, Robert A.; Sierawski, Brian D.; Lauenstein, Jean-Marie

Title: Failure Estimates for SiC Power MOSFETs in Space Electronics

Year: 2018

Version: Published version

**Copyright:** © 2018 by the authors. Licensee MDPI, Basel, Switzerland.

Rights: CC BY 4.0

Rights url: https://creativecommons.org/licenses/by/4.0/

### Please cite the original version:

Galloway, K. F., Witulski, A. F., Schrimpf, R. D., Sternberg, A. L., Ball, D. R., Javanainen, A., Reed, R. A., Sierawski, B. D., & Lauenstein, J.-M. (2018). Failure Estimates for SiC Power MOSFETs in Space Electronics. Aerospace, 5(3), 67. https://doi.org/10.3390/aerospace5030067





## Article Failure Estimates for SiC Power MOSFETs in Space Electronics

Kenneth F. Galloway <sup>1,\*</sup>, Arthur F. Witulski <sup>1</sup>, Ronald D. Schrimpf <sup>1</sup>, Andrew L. Sternberg <sup>1</sup>, Dennis R. Ball <sup>1</sup>, Arto Javanainen <sup>2,3</sup>, Robert A. Reed <sup>1</sup>, Brian D. Sierawski <sup>1</sup> and Jean-Marie Lauenstein <sup>4</sup>

- <sup>1</sup> Institute for Space and Defense Electronics, Department of Electrical Engineering and Computer Science, Vanderbilt University, VU Station B 351824, 2301 Vanderbilt Place, Nashville, TN 37235-1824, USA; arthur.f.witulski@vanderbilt.edu (A.F.W.); ron.schrimpf@vanderbilt.edu (R.D.S.); andrew.l.sternberg@vanderbilt.edu (A.L.S.); dennis.r.ball@vanderbilt.edu (D.R.B.); Robert.Reed@vanderbilt.edu (R.A.R.); brian.sierawski@vanderbilt.edu (B.D.S.)
- <sup>2</sup> Department of Electrical Engineering and Computer Science, Vanderbilt University, VU Station B 351824, 2301 Vanderbilt Place, Nashville, TN 37235-1824, USA; arto.javanainen@jyu.fi
- <sup>3</sup> Department of Physics, University of Jyvaskyla, P.O. Box 35, FI-40014 Jyvaskyla, Finland
- <sup>4</sup> NASA Goddard Space Flight Center, Code 561.4, Greenbelt, MD 20771, USA; Jean.M.Lauenstein@nasa.gov
- \* Correspondence: ken.galloway@vanderbilt.edu; Tel.: +1-615-343-0312

Received: 27 April 2018; Accepted: 20 June 2018; Published: 22 June 2018



**Abstract:** Silicon carbide (SiC) power metal-oxide-semiconductor field effect transistors (MOSFETs) are space-ready in terms of typical reliability measures. However, single event burnout (SEB) due to heavy-ion irradiation often occurs at voltages 50% or lower than specified breakdown. Failure rates in space are estimated for burnout of 1200 V devices based on the experimental data for burnout and the expected heavy-ion linear energy transfer (LET) spectrum in space.

**Keywords:** single event effects; heavy ions; silicon carbide; single-event burnout; power devices; power MOSFETs; reliability; failure rates

#### 1. Introduction

Silicon carbide (SiC) has excellent properties for power device applications. In comparison to silicon, it has higher breakdown field and higher thermal conductivity. SiC devices are ideally suited to high voltage, high power-density power converter applications, both on Earth and in space. These devices, in comparison to silicon devices, have advantages in breakdown voltage ( $\sim 10 \times$  Si), low on-resistance ( $\sim 1/100$  Si), high temperature operation ( $\sim 3 \times$  Si) and high thermal conductivity ( $\sim 10 \times$  Si) [1]. The typical terrestrial electrical reliability results for SiC power devices are excellent [2–6]. Advancements in the crystal quality of 4H-SiC substrates and epitaxial growth capabilities, improvements in 4H-SiC device gate oxides, and innovation in processing has yielded a significant commercial presence for SiC power metal-oxide-semiconductor field effect transistors (MOSFETs).

For spaceborne electronics, consideration must be given to the radiation response of a device. SiC power MOSFETs are affected by total ionizing dose (TID), but, typically, TID response is acceptable at a total dose <100 krad(Si), and, depending on the application, devices can be useful up to 300 krad(Si) [7]. However, the sensitivity of SiC power devices (both MOSFETs and diodes) to energetic heavy ions has been found to be higher than might be expected with both leakage current degradation and single event burnout being observed.

In this paper, we briefly discuss typical SiC power MOSFET reliability evaluations and the data for the degradation and burnout of 1200 V SiC power MOSFETs. Based on the available burnout versus

linear energy transfer (LET) experimental data and an approximation of the heavy-ion integrated LET spectrum expected in space, we estimate the worst-case failure rate for SiC power MOSETs in several space orbits.

#### 2. Brief Review of SiC Mosfet Electrical Reliability

Since their commercial introduction in about 2010, the reliability of SiC power MOSFETs has been carefully examined by a number of researchers [2–6]. Early on, it was expected that the weakest element in a 4H-SiC power MOSFET would be the MOS gate dielectric. However, improvements in materials and fabrication techniques have yielded reliable 4H-SiC gate oxides. A time dependent dielectric breakdown (TDDB) study of gate oxides in 4H-SiC showed that a 100-year lifetime at a temperature of 375 °C can be expected if the oxide electric field  $E_{ox}$  is lower than 3.9 MV/cm, suggesting that catastrophic failure of the gate oxide due to electrical stress is a manageable reliability issue for these devices [3]. High temperature tests on commercial devices from two sources indicated that the 1200 V SiC MOSFETs tested are capable of operating with junction temperatures over 250 °C for more than 500 h and an estimated  $5 \times 10^4$  h at 150 °C.

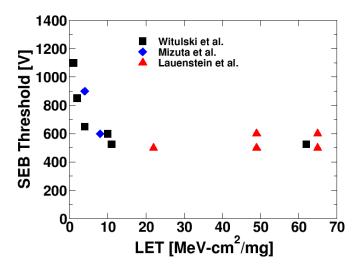
Accelerated life high temperature reverse bias (HTRB) tests and time dependent dielectric breakdown (TDDB) tests of gate oxides in 4H-SiC have been carried out on 1200 V production devices [5,6]. This work estimated TDDB mean time to failure values (MTTF) of nearly  $10^7$  h in continuous operation with 20 V applied to the gate. The HTRB test lifetime extrapolation yields over  $10^7$  h at 960 V continuous operation and somewhat more than  $10^5$  h at 1200 V. Field failure data are less than 5 FIT (1 FIT = 1 failure per billion hours) [5]. These reliability assessments [5,6] of commercially available devices indicate mean device lifetimes of more than 1000 years under normal device operating conditions.

Consequently, in terms of electrical reliability tests, SiC power MOSFETs are well suited for applications requiring high voltage and high power-density. However, for spaceborne electronics, the natural space radiation environment must be factored into the reliability considerations. We also note that these devices are susceptible to burnout due to terrestrial neutrons and their terrestrial radiation reliability has been evaluated by Lichtenwalner et al. [6] and Akturk et al. [8].

#### 3. Radiation Effects Data on 1200 V SiC Power MOSFET Devices

SiC power MOSFETs may undergo catastrophic single event burnout (SEB) when exposed to energetic heavy ions or protons [9–11]. Two types of single-event effects are observed when SiC power MOSFET devices are irradiated with heavy ions: degradation and catastrophic failure [10,11]. Figure 1 displays published single event burnout data from four different experiments examining SEB for 1200 V SiC power MOSFETs [9–11]. These 1200 V devices were manufactured by Wolfspeed (a CREE Company, Durham, NC, USA) [12]. There were differences in the on-resistance and current rating depending on the particular data set.

Note that, for LETs > 10 MeV-cm<sup>2</sup>/mg, all devices fail at reverse voltages significantly below the device rated voltage of 1200 V. In addition to SEBs, permanent damage to the device is manifested as an increase in drain leakage with the higher LET ions, similar to that observed in SiC Schottky barrier diodes [13]. No leakage current increase was observed for LETs <5 MeV-cm<sup>2</sup>/mg, including protons, before SEBs were observed. Witulski et al. [9] used technology computer-aided design (TCAD) simulations to demonstrate that turn-on of the parasitic bipolar transistor inherent in the SiC power MOSFET structure is part of the physical mechanism leading to catastrophic SEB failure in these devices. This is similar to what has been observed for SEB in silicon power MOSFETs.



**Figure 1.** Experimental measurements of single event burnout (SEB) threshold voltage versus heavy-ion linear energy transfer (LET) for 1200 V SiC Power metal-oxide-semiconductor field effect transistors (MOSFETs). Data from [9–11].

#### 4. Estimate of the Worst-Case Failure Rate for SiC Power MOSFETs in Space

Estimating the failure rate (FR), the failure in time or FIT rate, and the mean time between failures (MTBF), requires an estimate of the cross section for SEB failure and the flux of particles at an LET or higher that will cause this failure. Since SEB is a catastrophic failure, we use MTTF (mean time to failure) rather than MTBF. We will use a modification of the method proposed by Dashdondog et al. [14], based on worst-case assumptions. This method is similar to that of Titus et al. [15], as applied by Lauenstein et al. [16].

The 1200 V devices fail at approximately 500 V for any LET > 10 MeV-cm<sup>2</sup>/mg. Thus, the cross section for SEB failure is assumed to have reached its saturation values for LET > 10 MeV-cm<sup>2</sup>/mg. Since the bias boundary for SEB is almost constant for LET > 10 MeV-cm<sup>2</sup>/mg, we will define SEB failure as operation at a reverse voltage greater than 500 V for any LET > 10 MeV-cm<sup>2</sup>/mg. The failure rate (FR) under these conditions can be expressed as

$$FR = \sigma \int Flux(LET) \, dLET,$$

where  $\sigma$  is the saturated MOSFET SEB cross-section and the integral is evaluated over the ion flux as a function of LET. The integral is to be evaluated for LETs greater than 10 MeV-cm<sup>2</sup>/mg (the cross section is assumed to be zero for LET values lower than the SEB threshold value). The value of this integral depends on the spacecraft mission or orbit and solar activity. To estimate the failure rate for SiC power MOSETs in space, we make some assumptions about the SEB cross-section and the integrated LET spectrum expected.

The devices tested by Witulski et al. [9] and represented by the data points in Figure 1 have a chip size of approximately 2 mm by 3 mm. Since the cross-section for SEB is not known from experiments, a simple approximation is made based on the chip area ( $6 \times 10^{-2}$  cm<sup>2</sup>). However, the entire chip is not sensitive. In addition, the time when the device is reverse biased is less than 100%. Taking the sensitive area as 50% of the die area and the duty-cycle to be 50%, this reduces the effective cross-section, independent of LET, to

$$\sigma = 1.5 \times 10^{-2} \text{ cm}^2$$
.

For our estimates, we use LET distributions from Xapsos et al. [17] and from CREME96 [18] as shown in Figure 2. The Xapsos et al. spectrum is based on a probabilistic model of cumulative solar heavy ion spectra behind 100 mils of aluminum during a solar maximum time period. The potential

impact of inelastic proton interactions is not considered. Only a very small fraction of the recoils from proton nuclear reactions have LETs >  $10 \text{ MeV cm}^2/\text{mg}$  and would most likely contribute a negligible amount to the failure rate [19,20].

The estimate here is based on the 99% confidence level curve, which Xapsos et al. consider as appropriate for use as a conservative design estimate of the single-event rate due to solar particles during solar max. The integral for LET greater than 10 MeV-cm<sup>2</sup>/mg results in

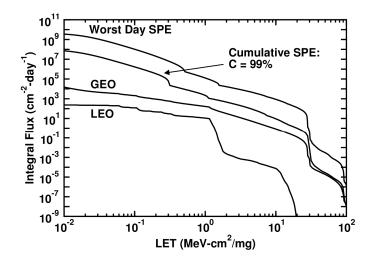
$$\int Flux(LET) dLET = 10 \text{ cm}^{-2} \text{ day}^{-1}.$$

This choice of effective cross section and of an integrated LET spectrum yields

FR = 
$$6.25 \times 10^{-3}$$
/h and FIT =  $6.25 \times 10^{6}$ .

This FIT value leads to a mean time to failure (MTTF) of 160 h. This estimate, however, neglects a number of factors including possible angular effects.

SEB is most likely for an ion strike near normal to the top surface of the device. There is no published data on the angular effects in SiC power MOSFETs. If we assume that the device is sensitive to SEB for strikes  $\pm 15^{\circ}$  to normal [21,22] and that in the spacecraft the environment is isotropic, only about 1/67 of the  $4\pi$  steradian geometry is available for SEB. This leads would to a reduction in the calculated FIT by a factor of approximately 70. Even if we assume that the FIT is too high by a factor of 70, the MTTF is still on the order of 11,200 h or less than 1.5 years. This is a high FIT rate compared to that due to other reliability concerns. The range of FITs for most components is 100 to 1000, based on electrical reliability.



**Figure 2.** Worst day solar particle event (SPEW) from CREME96 [18]. GEO and LEO are solar minimum spectra from CREME96 [18]. Cumulative solar particle event spectra at the 99% confidence levels after Xapsos et al. [17]. Results for 100 mils aluminum shielding. GEO—geostationary orbit, often referred to as a geosynchronous equatorial orbit (GEO); LEO—low Earth orbit, common usage in discussing satellites.

If we consider the worst day solar particle spectrum as given by CREME96 [18], which is based on the 1989 event averaged over one day as determined from GOES–6 and –7 instrumentation, we obtain

$$\int Flux(LET) dLET = 10^3 \text{ cm}^{-2} \text{ day}^{-1},$$

which yields

$$FIT = 6.25 \times 10^8$$
 and  $MTTF = 1.6$  h

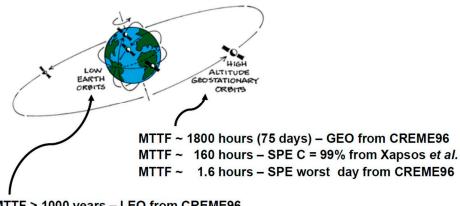
Table 1 tabulates the integral evaluated for LET greater than 10 MeV-cm<sup>2</sup>/mg behind 100 mils of aluminum shielding, the worst-case FIT and the MTTF for the episodic cases considered above and for two additional long-term average environments (GEO, LEO) as calculated using CREME96 [18].

Spectrum	Int. (no./cm <sup>2</sup> -Day)	FIT (1 Per Billion Hours)	MTTF (Hours)
SPEW	1000	$6.25  imes 10^8$	1.6
SPE	10	$6.25  imes 10^6$	160
GEO	0.9	$5.6 imes10^5$	1786
LEO	$1 imes 10^{-4}$	62.5	$1.6  imes 10^7$

**Table 1.** Value of integral LET spectra (Int.) and SiC MOSFET FIT and MTTF for several space scenarios.All results assume 100 mils of aluminum shielding.

SPEW = worst day solar particle event (SPE) from CREME96; SPE = cumulative SPE at solar max., 99% confidence from Xapsos; GEO = geostationary Earth orbit during solar min. from CREME96; LEO = low Earth orbit during solar min. from CREME96.

As seen from the values in Table 1, the FIT and MTTF vary significantly with mission and solar activity. In low earth orbit with typical solar activity, the devices should operate for years. However, in a worst case solar event, the device may only last a few hours, if that long. Results given in the table are illustrated in Figure 3. In GEO, the devices are expected to fail in relatively short times. In low earth orbit with typical solar activity, the devices should operate for years. In LEO at a solar max, you might expect the time to failure to be reduced, but the ion flux will be modified by the earth's magnetosphere.



MTTF > 1000 years – LEO from CREME96

**Figure 3.** SEB failure rate for two potential orbits for a 1200 V SiC power MOSFET. Image from Department of Commerce, National Oceanic and Atmospheric Administration.

#### 5. Discussion

In this work, we have considered 1200 V SiC MOSFET devices and estimated the failure rate in the space radiation environment for several different possible missions. Experimental results have demonstrated that these devices fail at approximately 500 V for any LET >10 MeV-cm<sup>2</sup>/mg. We estimate the SEB failure rate based on a constant cross section for operation at a reverse voltage greater than 500 V for any LET > 10 MeV-cm<sup>2</sup>/mg.

A number of assumptions have been made to arrive at the estimates given above in Table 1 and the uncertainty in these estimates for FIT and MTTF may be relatively large. Even so, regardless of the assumptions, it appears that SiC power MOSFETs have the potential for significant failure rates due to heavy ion radiation exposure in the space radiation environment.

There may be situations where size, weight, and power requirements justify the use of SiC power MOSFETs in space. The Orion multi-purpose crew vehicle (MPCV) was developed by NASA to send crews of up to six astronauts on missions through space that could last as long as six months. As a secondary mission, the craft will also be used as a backup vehicle for cargo and crewed missions to the International Space Station. In addition, 1200 V SiC power MOSFETs are being used for this system [23]; however, these devices are being used with the voltage significantly derated. Additionally, SiC power MOSFETs are in use for the NASA MMS (Magnetospheric Multiscale) spacecraft.

This worst-case analysis indicates that, at this time, thorough heavy ion testing of any commercially available SiC MOSFET component proposed for utilization in spaceborne electronic systems is needed. Currently, any use of 1200 V SiC MOSFETs would require significant de-rating. A 1200 V device should not be used at voltages above 500 V in any radiation environment with LETs > 10 LET >10 MeV-cm<sup>2</sup>/mg.

Author Contributions: A.L.S., A.J., and J.-M.L. provided experimental data and insights; K.F.G., A.F.W., R.D.S., D.R.B., R.A.R. and B.D.S. contributed to the analysis; K.F.G. wrote the paper.

**Funding:** Support for this work at Vanderbilt provided by NASA ESI Grant No: NNX17AD09G, at NASA Goddard by the NEPP Program, and at University of Jyväskylä by ESA/ESTEC Contract No. 4000111630/14/NL/PA and Academy of Finland Project No. 2513553.

**Acknowledgments:** We would like to thank Mike Alles and Robert Johnson at Vanderbilt, Ari Virtanen at the University of Jyvaskyla, Dave Grider, Dan Lichtenwalner, and Brett Hull at Wolfspeed, and Ray Ladbury and Mike Xapsos at NASA Goddard Space Flight Center for useful discussions. This manuscript is based on a presentation at the 2018 HEART Technical Interchange Meeting, Tucson, AZ, USA, 16–17 April 2018.

Conflicts of Interest: The authors declare no conflict of interest.

#### References

- Elasser, A.; Chow, T.P. Silicon Carbide Benefits and Advantages for Power Electronic Circuits and Systems. Proc. IEEE 2002, 90, 969–986. [CrossRef]
- Ryu, B.; Hull, B.; Dhar, S.; Cheng, L.; Zhang, Q.; Richmond, J.; Das, M.; Agarwal, A.; Palmour, J.; Lelis, A.; et al. Performance, Reliability, and Robustness of 4H-SiC Power DMOSFETs. *Mater. Sci. Forum* 2010, 645, 969–974. [CrossRef]
- Yu, L.C.; Dunne, G.T.; Matocha, K.S.; Cheung, K.P.; Suehle, J.S.; Sheng, K. Reliability Issues of SiC MOSFETs: A Technology for High-Temperature Environments. *IEEE Trans. Device Mater. Reliab.* 2010, 10, 418–426. [CrossRef]
- Maxime, B.; Remy, O.; Thibault, C.; Pierre, P.; Sebestion, O.; Dominique, T. Electrical performances and reliability of commercial SiC MOSFETs at high temerpature and in SC conditions. In Proceedings of the 2015 17th European Conference on Power Electronics and Applications (EPE'15 ECCE-Europe), Geneva, Switzerland, 8–10 September 2015.
- Gajewski, D.S.; Hull, B.; Lichtenwalner, D.J.; Ryu, S.-H.; Bonelli, E.; Mustain, H.; Wang, G.; Allen, S.T.; Palmour, J.W. SiC Power Device Reliability. In Proceedings of the 2016 IEEE International Integrated Reliability Workshop (IIRW), South Lake Tahoe, CA, USA, 9–13 October 2016; pp. 29–34.
- Lichtenwalner, D.J.; Hull, B.; Van Brunt, E.; Sabri, S.; Gajewski, D.A.; Grider, D.; Allen, S.; Palmour, J.W.; Akturk, A.; McGarrity, J. Reliability Studies of SiC Vertical Power MOSFETs. In Proceedings of the 2018 IEEE International Reliability Physics Symposium (IRPS), Burlingame, CA, USA, 11–15 March 2018; pp. 2B.2-1–2B.2-6.
- Akturk, A.; McGarrity, J.M.; Potbhare, S.; Goldsman, N. Radiation Effects in Commercial 1200 V 24 A Silicon Carbide Power MOSFETs. *IEEE Trans. Nucl. Sci.* 2012, *59*, 3258–3264. [CrossRef]
- 8. Akturk, A.; Wilkins, R.; McGarrity, J.; Gersey, B. Single Event Effects in Si and SiC Power MOSFETs Due to Terrestrial Neutrons. *IEEE Trans. Nucl. Sci.* 2017, 64, 529–535. [CrossRef]
- 9. Witulski, A.F.; Ball, D.R.; Galloway, K.F.; Javanainen, A.; Lauenstein, J.-M.; Sternberg, A.L.; Schrimpf, R.D. Analysis of SEB Physics in SiC Power MOSFETs. *IEEE Trans. Nucl. Sci.* **2018**, in press.

- Lauenstein, J.-M.; Casey, M.C.; Topper, A.D.; Wilcox, E.P.; Phan, A.M.; LaBel, K.A. Silicon Carbide Power Device Performance Under Heavy-Ion Irradiation. In Proceedings of the IEEE Nuclear and Space Radiation Effects Conference, Boston, MA, USA, 13–17 July 2015.
- 11. Mizuta, E.; Kuboyama, S.; Abe, H.; Iwata, Y.; Tamura, T.; Device, A.S. Investigation of Single-Event Damages on Silicon Carbide (SiC) Power MOSFETs. *IEEE Trans. Nucl. Sci.* **2014**, *61*, 1924–1928. [CrossRef]
- 12. SiC and GaN Power and RF Solutions. Available online: https://www.wolfspeed.com/ (accessed on 25 April 2018).
- 13. Javanainen, A.; Galloway, K.F.; Nicklaw, C.; Bosser, A.L.; Ferlet-Cavrois, V.; Lauenstein, J.M.; Pintacuda, F.; Reed, R.A.; Schrimpf, R.D.; Weller, R.A.; et al. Heavy Ion Induced Degradation in SiC Schottky Diodes: Bias and Energy Deposition Dependence. *IEEE Trans. Nucl. Sci.* **2017**, *64*, 415–420. [CrossRef]
- 14. Dashdondog, E.; Harada, S.; Shiba, Y.; Omura, I. Failure rate calculation method for high power devices in space applications at low earth orbit. *Microelectron. Reliab.* **2016**, *84*, 502–506. [CrossRef]
- 15. Titus, J.L.; Wheatley, C.F.; Wheatley, T.H.; Levinson, W.A.; Burton, D.I.; Barth, J.L.; Reed, R.A.; LaBel, K.A.; Howard, J.W.; van Tyne, K.M. Prediction of Early Lethal SEGR Failure of VDMOSFETs for Commercial Space Systems. *IEEE Trans. Nucl. Sci.* **1999**, *46*, 1640–1651. [CrossRef]
- Lauenstein, J.-M.; Goldsman, N.; Liu, S.; Titus, J.L.; Ladbury, R.L.; Kim, H.S.; Phan, A.M.; LaBel, K.A.; Zafrani, M.; Sherman, P. Effects of Ion Atomic Number on Single-Event Gate Rupture (SEGR) Susceptibility of Power MOSFETs. *IEEE Trans. Nucl. Sci.* 2011, *58*, 2628–2636. [CrossRef]
- 17. Xapsos, M.A.; Stauffer, C.; Jordan, T.; Barth, J.L.; Mewaldt, R.A. Model for Cumulative Solar Heavy Ion Energy and Linear Energy Transfer Spectra. *IEEE Trans. Nucl. Sci.* **2007**, *54*, 1985–1989. [CrossRef]
- 18. CRÈME Site. Available online: https://creme.isde.vanderbilt.edu/ (accessed on 25 April 2018).
- 19. Peterson, E.L. Soft errors due to protons in the radiation belt. *IEEE Trans. Nucl. Sci.* **1981**, *28*, 3981–3986. [CrossRef]
- 20. Reed, R.A.; Marshall, P.W.; Kim, H.S.; McNulty, P.J.; Fodness, B.; Jordan, T.M.; Reedy, R.; Tabbert, C.; Liu, M.S.T.; Heikkila, W.; et al. Evidence for Angular Effects in Proton-Induced Single-Event Upsets. *IEEE Trans. Nucl. Sci.* **2002**, *49*, 3038–3044. [CrossRef]
- 21. Javanainen, A.; Turowski, M.; Galloway, K.F.; Nicklaw, C.; Ferlet-Cavrois, V.; Bosser, A.; Lauenstein, J.-M.; Muschitiello, M.; Pintacuda, F.; Reed, R.A.; et al. Heavy-Ion-Induced Degradation in SiC Schottky Diodes: Incident Angle and Energy Deposition Dependence. *IEEE Trans. Nucl. Sci.* **2017**, *64*, 2031–2037. [CrossRef]
- 22. Lauenstein, J.-M.; NASA Goddard Space Flight Center, Greenbelt, MD, USA. Private Communication, unpublished work.
- Lauenstein, J.-M.; Casey, M.C.; Samsel, I.; LaBel, K.A.; Chen, Y.; Ikpe, S.; Wilcox, T.; Phan, A.; Kim, H.; Topper, A.D. Silicon Carbide Power Devices and Integrated Circuits. In Proceedings of the NASA Electronic Parts and Packaging Program (NEPP) Electronics Technology Workshop (ETW), Greenbelt, MD, USA, 26–29 June 2017.



© 2018 by the authors. Licensee MDPI, Basel, Switzerland. This article is an open access article distributed under the terms and conditions of the Creative Commons Attribution (CC BY) license (http://creativecommons.org/licenses/by/4.0/).